The Max-LUT Method: Mutual-Information Maximizing Lookup Tables



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Hardware-Aware Information Theory



ETH Zurich http://bit.ly/2nTEfCy

LDPC codes are widely used. In communications:

- 5G, WiFi 802.11n, video broadcasting
- Ethernet over twisted pair
- In data storage:
- flash memories and SSD drives

Numerous ECC decoding algorithms:

• implemented in VLSI

• "ad hoc" implementation by engineers Can we give a theoretical foundation to implementations?







Conventional Algorithm Design Process







From Human-Designed Decoding Algorithms, to Machine-Assisted Ones



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Machine Learning for Coding



Final Progarm | Ad-hoc Meeting on Deep Learning & Coding | TAU, Israel, June 19, 2017

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Ad-hoc Meeting on Deep Learning & Coding, TAU, Israel, June 19, 2017



Ad-hoc Meeting on Deep Learning & Coding will take place at Tel Aviv University, Israel, on Monday, June 19, 2017.

Location: Room 011, Classrooms (Kitot) Building, Faculty of Engineering - see at-

tached map & link.

The program below includes talks that will review and update on the current research covered by papers published during the last year on Deep Learning & Coding - see link below for Abstracts & References.

These talks are free and open to all interested faculty & students, as well as for participants from research institutions and industry.



Problem Setting

Design fixed precision LDPC decoders:

- Everything is discrete: channel, messages, nodes
- Nodes implement discrete mappings

> find these decoding mappings by maximizing mutual information

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Outline

1 Factor graphs and Conventional VLSI Implementation

- Factor graphs
- Quantization of factor graph messages

2 Max-LUT Method

- Quantization of factor graph messages
- Max-LUT method

3 Application to LDPC code decoding

- **4 Discussion**

• Numerical results: 4 bits/message "performs like floating point"





1 Factor Graph Representation of Decoders



"messages" are just numbers, "nodes" compute functions

output: transmitted information estimate

Factor Graphs: Local Functions

Check node local function $x_1 + x_2 + x_3 + x_4 = 0$ or $f(x_1, x_2, x_3) = x_4$

codeword x

Factor Graphs: Update Rules

Variable node update rule:

 $L_v =$

 $R_c = -2 \tanh^-$

Final output

$$Y_v + \sum_{i \in \mathcal{N}(v) \setminus v} V_i$$

$$\frac{1}{i \in \mathcal{M}(c) \setminus c} \tanh\left(-\frac{L_i}{2}\right)$$

$$\widehat{L}_v = Y_v + \sum_{i \in \mathcal{N}(v)} V_i$$

Quantization in VLSI Receivers

Desired: efficient quantization schemes

VLSI quantization schemes are chosen ad hoc way by engineers

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Max-LUT is a method for implementing the node decoder functions for graph-based decoders, using lookup tables that maximize mutual information.

Characteristics of the Max-LUT Method

- We need a factor graph
- We need an input distribution
- Factor graph messages are discrete
- Decoding functions are look up tables (LUT)
- Lookup tables are designed to maximize mutual information

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Lookup Table (LUT) Implementation

Small LUTS are easily implemented in VLS1

Max-LUT Method: Central Idea

Encoder Side. Code symbols X

- Check node $x_3 = x_1 + x_2$
- Var node $x_1 = x_2 = x_3$

• etc.

Choose LUT to maximize mutual information $\max I(X_3; Z) = \max_{LUT} I(X_3; LUT(L_1, L_2))$

Decoder Side

 L_i is a noisy version of X_i , Z is a noisy version of X_3

Max-LUT Method: Three Steps

- Step 1: Find joint distribution from marginal distributions
- Step 2: Quantize joint distribution maximize mutual information
- Step 3: Find LUT from the quantizer Example
 - LDPC variable node, two inputs L_1, L_2 with $Pr(L_i|X_i)$ • local constraint: " $x_1 = x_2 = x_3$ "

 - Goal: find max-MI lookup table $Z = LUT(L_1, L_2)$

Max-LUT Step 1: Joint Distribution

 $\Pr(\mathsf{L}_1|\mathsf{X}_1)$ $\mathsf{L}_1 \in \{1, 2, 3, 4, 5, 6\}$

Max-LUT Step 2: Quantize

- Too many levels! Reduce to **Z** with *K* levels
- Quantizer is a mapping from (L_1, L_2) to Z

Max-LUT Step 3: Lookup Table

Lookup table: $Z = LUT(L_1, L_2)$

Max-LUT Method in One Slide

	Z		
L	1	2	3
1	4	4	3
2	5	5	5
3	5	5	4
4	3	3	2
5	2	2	1
6	1	1	1

Lookup table (mapping)

3 Application to LDPC Code Decoding

• How to obtain the densities needed by Max-LUT method?

> Density evolution

- How to keep the lookup table reasonable size? > Node decomposition or "opening the node"
- How does it perform numerically?
 - > Similar to BP with four bits/message

Node Decomposition (Opening the node) Reduces size of lookup table

Example: $d_c = 6$ and for the lookup table $\Delta = 3$ bits per message

$$2^{(d_c - 1)\Delta} = 32768$$

Memory locations for $\psi_1^{(\ell)}, ..., \psi_4^{(\ell)}$

 $2^{(d_c-1)\Delta}$

 $(d_c - 2)2^{2\Delta} \quad [(d_c - 2)2^{2\Delta} = 4 \times 64]$ = 256

FIGURE 8. BER and WER results for the LUT decoding algorithm. $d_v = 4$, $d_c = 9$, R = 0.56, N = 4113, Max. Iter.= 30, Array code [2]. The numbers on the graph represent the average number of iteration per $E_b/N0$.

BI-AWGN: Lower Error Floors, Fewer Iterations

 $N = 2048, (d_v = 6, d_c = 32), R = 0.84$ and Max. iter = 30 This code is used in IEEE 802.3an 10GBase-T standard producing an operation of 10 Gb/s.

The proposed decoding mapping functions

- using10 iterations can achieve the same BER performance than full SPA using 30 iterations.
- using 30 iterations can surpass the BER performance of full SPA using 30 iterations.

4 Discussion

- Where are the LLRs?
- Asymmetric or arbitrary noise models
- BCJR algorithm/turbo decoding

Where are the LLRs?

You can't just consider the LLRs quantization, you have to consider their distribution

> Actually, we don't even consider the LLRs, we only consider the distribution

algorithm engineer

But then what are the LLRs?

Only distributions are needed to compute mutual information.

Instead of LLRs, we just use integers $\{1, 2, ..., K\}$

Here are the LLRs: After quantization

Can find the LLRs since we know the probability distributions.

Iterations

- Max-LUT method does not assume symmetric noise
- Density evolution using Pr(y | X = 0) and Pr(y | X = 1)• Generates lookup-tables optimized for asymmetric noise
- Suitable for flash memories and non-linear wireless channels.

BCJR algorithm/turbo decoding

Factor graph for Markov chains (including time-varying) Convolutional codes, turbo codes, intersymbol interference channels

F R Kschischang et al, "Factor Graphs and the Sum-Product Algorithm," IT Trans, Feb. 2001

Conclusion: Did we make an "algorithm that designs algorithms"?

image: https://www.mindsonar.info http://bit.ly/2nbSXrp

