

# Antichain algorithm, its theory and applications

By Pakorn Techaveerapong

A thesis submitted to  
School of Information Science,  
Japan Advanced Institute of Science and Technology,  
in partial fulfillment of the requirements  
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Master of Information Science  
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Written under the direction of  
Professor Mizuhito Ogawa

September, 2013

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# Abstract

The antichain algorithm and bounded model checking (BMC) are well-known techniques in finite model checking framework. However, they are less explored for pushdown model checking. In this thesis, we combine them together in order to check universality of visibly pushdown automata (VPAs). VPAs, introduced by Alur and Madhusudan in 2004, is a subclass of pushdown automata whose stack behavior is determined by the partition of input alphabet. VPAs have been applied to be useful in various contexts, e.g., processing XML documents and checking context-sensitive properties. Due to its high complexity, ExpTime-complete, the universality of VPAs is a challenge. We propose two approaches to check universality of VPAs. We implement the proposed approaches in a prototype tool and conduct experiments on randomly generated VPAs. Although the experimental results show that our algorithms are inferior to the other algorithms, we can prove a huge contribution of antichain algorithm in BMC.

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# Chapter 1

## Introduction

For a finite partially ordered set, an antichain in  $P$  is a set of pairwise incomparable elements (i.e., no two different elements in subset of  $P$  are related). Antichains are also called Sperner systems in literature [18].

In automata theory, antichains are used to get smaller objects to manipulate and to reduce the number of computation by reducing an explicit determinization step. Recently, antichains have been successfully applied to many decision problems related to automata: universality and inclusion for finite word automata [19, 22], for Büchi automata [21], and for non-deterministic bottom-up tree automata [13]. In this research project, we are focusing on uses of antichain for universality checking of visibly pushdown automata [37].

Visibly pushdown automata (VPAs) [5], aka nested word automata [6], are pushdown automata whose stack behavior (i.e., whether to push, pop, or no stack operation) is determined by an input symbol according to a fixed partition of input alphabet. This class of visibly pushdown automata enjoys many good properties similar to those of the class of finite automata. It is closed under all boolean operations. The main reason is that, each non-deterministic VPA can be transformed into an equivalent deterministic one. As a result, VPAs appear to be useful in many contexts of model-checking framework ,e.g., as automaton models for processing XML streams [33, 30], as AOP protocols for component-based systems [32], and as semantics of programming languages [2, 31] and verification [3].

To check universality of a non-deterministic VPA  $M$  over the input alphabet  $\Sigma$  (i.e., to check if  $L(M) = \Sigma^*$ ) is EXPTIME-complete. The standard method faces a hardness due to the expensive determinization process because determinization for VPA requires exponential blowup [5].

There are many attempts to tackle the universality problem of VPA. Nguyen [38] proposed a better-than-standard algorithm. This algorithm simultaneously performs an on-demand determinization and reachability checking by  $\mathcal{P}$ -automaton [12, 27]. Later Nguyen and Ohsaki [37] improved it by introducing antichains over transitions of  $\mathcal{P}$ -automata, to generate the smallest number of reachable configurations. Bruyere, et al., [14] also proposed an universality checking algorithm with antichains but their algorithm is alternative to Nguyen's. They do not use the regularity of the set of reachable configurations but they observe on the bottom-up unranked trees. In [28], the authors provide a solution for checking universality of VPAs over finite and infinite words. They avoid the determinization and complementation steps, and use Ramsey-based universality checking algorithm. Their algorithm do not seem to use antichains.

Bounded model checking (BMC) [9, 16] is a symbolic counter-example-finding method that searches for a violation of a given property in a model structure. BMC approach looks for counter-examples of bounded length. If no counter-examples are found, enlarge a bound and repeat the search again. Theoretically, BMC may have high-level complexity, but by combining SAT-encoding and recent SAT-solvers, it reduces the space complexity a lot. In practice, BMC often outperforms the BDD-based symbolic model checking [15, 10].

An important issue of BMC is to make it *complete*. Because at such a bounded length, absence of counter-example is still unclear. From this reason, many attempts have been tried to turn BMC into a complete method with the ability to guarantee the absence of counter-example [9, 17]. Typically, the reachability diameter (length of longest shortest path) and the reachability recurrence diameter (length of longest loop-free path) are used to be the completeness threshold of many properties [29].

There is an example of using BMC in model-checking of pushdown automata. In [8], Basler et al. proposed an algorithm for checking reachability of pushdown systems using BMC to compute universal summaries [7].

The original contribution of this research consists in defining two BMC algorithms for checking universality of VPAs. We call them BMCi and BMCp. These algorithms are combinations of antichain and BMC by using antichain to reduce the size of search space, and the number of variables and clauses of propositional formulae.

We ran tests on randomly generated VPAs. Although, the experimental results have shown that our algorithms are still far from the original antichain algorithm in [37], we can see a huge improvement when compare between BMC with antichain and BMC without antichain.



In Chapter 2 we recall basic notions and properties of VPAs, and  $\mathcal{P}$ -automata technique and correctness proof. Chapter 3 presents an antichain algorithm for checking universality of VPA. Furthermore, the correctness proof of the algorithm is also given. We give some approaches of BMC for VPA and analyze possibilities in Chapter 4. Based on Chapter 4, our BMC approaches for universality checking of VPAs are proposed in Chapter 5. We implement and carry out some relevant experiments, the experimental results and observation are presented in Chapter 6. We discuss some related works in Chapter 7. Finally, we conclude the research and give suggestions for future works in Chapter 8.

# Chapter 2

## Preliminaries

### 2.1 Visibly pushdown automata (VPA)

Let  $\Sigma$  be the finite input alphabet, and let  $\Sigma = \Sigma_c \cup \Sigma_i \cup \Sigma_r$  be a partition of  $\Sigma$  where  $\Sigma_c$ ,  $\Sigma_i$  and  $\Sigma_r$  are the disjoint finite set of call (push) alphabets, internal alphabets and return (pop) alphabets, respectively. Visibly pushdown automata are formally defined as follows:

**Definition 1** (VPA. [5]). *A visibly pushdown automata (VPA)  $M$  over input alphabet  $\Sigma$  is a tuple  $(Q, Q_0, \Gamma, F, \Delta)$  where  $Q$  is a finite set of states,  $Q_0 \subseteq Q$  is a set of initial states,  $\Gamma$  is a finite stack symbols with a special symbol  $\perp$  (so-called bottom of stack),  $F \subseteq Q$  is a set of final states, and  $\Delta = \Delta_c \cup \Delta_i \cup \Delta_r$  is the transition relation where  $\Delta_c \subseteq Q \times \Sigma_c \times Q \times (\Gamma \setminus \{\perp\})$ ,  $\Delta_i \subseteq Q \times \Sigma_i \times Q$  and  $\Delta_r \subseteq Q \times \Sigma_r \times \Gamma \times Q$*

- A transition  $(q, c, q', \gamma) \in \Delta_c$  where  $c \in \Sigma_c$  and  $\gamma \neq \perp$ , is a push-transition where on reading  $c$  regardless of stack, state changes from  $q$  to  $q'$  and  $\gamma$  is pushed onto the stack; we denote a transition by  $q \xrightarrow{c/+ \gamma} q'$ .
- A transition  $(q, i, q') \in \Delta_i$  where  $i \in \Sigma_i$ , is a internal-transition where on reading  $i$  regardless of stack, state changes from  $q$  to  $q'$ ; we denote a transition by  $q \xrightarrow{i} q'$ .
- A transition  $(q, r, \gamma, q') \in \Delta_r$  where  $r \in \Sigma_r$ , is a pop-transition where on reading input  $r$  and top of stack  $\gamma$ , state changes from  $q$  to  $q'$  and  $\gamma$  is popped (if top is  $\perp$ , read but not popped); we denote a transition by  $q \xrightarrow{r/- \gamma} q'$  (or  $q \xrightarrow{r/\perp} q'$ ).

A *stack* is a non-empty finite string over  $\Gamma$  ending with the bottom-of-stack symbol  $\perp$ . We write  $St$  for the set of all stacks.  $St$  is denoted as  $St = \{w\perp \mid w \in (\Gamma \setminus \{\perp\})^*\}$ . A configuration is a pair  $(q, \sigma)$  such that  $q \in Q$  and  $\sigma \in St$ . The transition relation of VPA

can be used to define how the configuration of the machine changes in a single step: we say  $(q, \sigma) \xrightarrow{a} (q', \sigma')$  if one of the following holds:

- If  $a \in \Sigma_c$ , then there exists  $\gamma \in \Gamma \setminus \{\perp\}$  such that  $q \xrightarrow{a/\gamma} q'$  and  $\sigma' = \gamma \cdot \sigma$ .
- If  $a \in \Sigma_i$ , then  $q \xrightarrow{a} q'$  and  $\sigma' = \sigma$
- If  $a \in \Sigma_r$ , then either there exists  $\gamma \in \Gamma \setminus \{\perp\}$  such that  $q \xrightarrow{a/\gamma} q'$  and  $\sigma = \gamma \cdot \sigma'$  or  $q \xrightarrow{a/\perp} q'$  and  $\sigma' = \sigma = \perp$

*Acceptance.* For a word  $w = a_1 \dots a_n$  in  $\Sigma^*$ , a run of  $M$  on  $w$  is a sequence of configurations  $(q_0, \sigma_0) \xrightarrow{a_1} (q_1, \sigma_1) \xrightarrow{a_2} \dots \xrightarrow{a_n} (q_n, \sigma_n)$  where each  $q_i \in Q, \sigma_i \in St, q_0 \in Q_0$  and  $\sigma_0 = \perp$ , and is denoted by  $(q_0, \sigma_0) \xrightarrow{w}^* (q_n, \sigma_n)$ . A run  $(q_0, \sigma_0) \xrightarrow{w}^* (q_n, \sigma_n)$  is accepting if the last state is a final state, i.e. if  $q_n \in F$ . A word  $w \in \Sigma^*$  is accepted by a VPA  $M$  if there is an accepting run of  $M$  on  $w$ . The language of  $M$ ,  $L(M)$ , is the set of words accepted by  $M$ . The language  $L \subseteq \Sigma^*$  is a visibly pushdown language (VPL) if there exists a VPA  $M$  with  $L = L(M)$ .

**Definition 2** (deterministic VPA. [5]). *A VPA  $M$  is deterministic if  $|Q_0| = 1$  and for every state  $q \in Q$ :*

- for every  $a \in \Sigma_c$ , there is at most one stack symbol  $\gamma \in \Gamma$  and at most one transition of the form  $(q, a, q', \gamma) \in \Delta_c$
- for every  $a \in \Sigma_i$ , there is at most one transition of the form  $(q, a, q') \in \Delta_i$
- for every  $a \in \Sigma_r$  and  $\gamma \in \Gamma$ , there is at most one transition of the form  $(q, a, \gamma, q') \in \Delta_r$

As shown in [5], any non-deterministic VPA can be transformed into an equivalent deterministic one. The main idea behind the construction is to do a subset construction but store the call transitions and simulate them later, that is at the time of the corresponding pop transition. The construction has two components: a set of *summary edges*  $S$ , that keeps track of what transitions are possible from the last push transition to the corresponding pop transition, and a set of *path edges*  $R$ , that keeps track of all possible states reached from initial states (see [5] for more detail). However, this construction contains unnecessary information, that is the set of summaries  $S$  may contain pairs that are not truly reached from initial states. In [38], Nguyen has modified the algorithm to make every pair  $(S, R)$  satisfy  $\Pi_2(S) = R$  without disturbing the correctness of the algorithm (where  $\Pi_2(S) = \{s' \mid (s, s') \in S\}$  is the projection on the second component). After this modification, the construction is able to keep information as small as possible, that is every pair in  $S$  keeps only reachable states and the component  $R$  is no longer needed.

---

**Algorithm 1:** Determinization for VPA

---

**Data:** A non-deterministic VPA  $M = (Q, Q_0, \Gamma, F, \Delta)$

**Result:** An equivalent determinized VPA  $M^d = (Q', Q'_0, \Gamma', F', \Delta')$

**begin**

$$Q' = 2^{Q \times Q}$$

$$Q'_0 = \{Id_{Q_0}\}$$

$$\Gamma' = Q' \times \Sigma_c$$

$$F' = \{S \in Q' \mid \Pi_2(S) \cap F \neq \emptyset\}$$

the transition relation  $\Delta' = \Delta'_c \cup \Delta'_i \cup \Delta'_r$  is given by:

– **Push:** For every  $a \in \Sigma_c, S \xrightarrow{a/+(S,a)} Id_{R'} \in \Delta'_c$  where

$$R' = \{q' \mid \exists q \in \Pi_2(S) : q \xrightarrow{a/+\gamma} q' \in \Delta_c\}$$

– **Internal:** For every  $a \in \Sigma_i, S \xrightarrow{a} S' \in \Delta'_i$  where

$$S' = \{(q, q') \mid \exists q'' \in Q : (q, q'') \in S, q'' \xrightarrow{a} q' \in \Delta_i\}$$

– **Pop:** For every  $a \in \Sigma_r,$

- if stack is empty:  $S \xrightarrow{a/\perp} S' \in \Delta'_r$  where

$$S' = \{(q, q') \mid \exists q'' \in Q : (q, q'') \in S, q'' \xrightarrow{a/\perp} q' \in \Delta_r\}$$

- if stack is not empty:  $S \xrightarrow{a/-(S',a')} S'' \in \Delta'_r$  where

$$update = \{(q, q') \mid \exists q_1, q_2 \in Q : (q_1, q_2) \in S, q \xrightarrow{a'/+\gamma} q_1 \in \Delta_c, q_2 \xrightarrow{a'/-\gamma} q' \in \Delta_r\}$$

$$S'' = \{(q_3, q') \mid \exists q \in Q : (q_3, q) \in S', (q, q') \in update\}$$

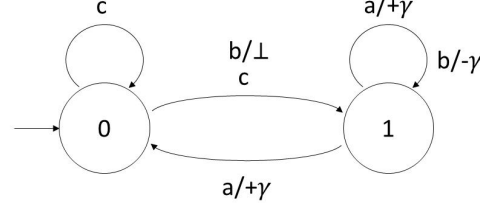
**end**

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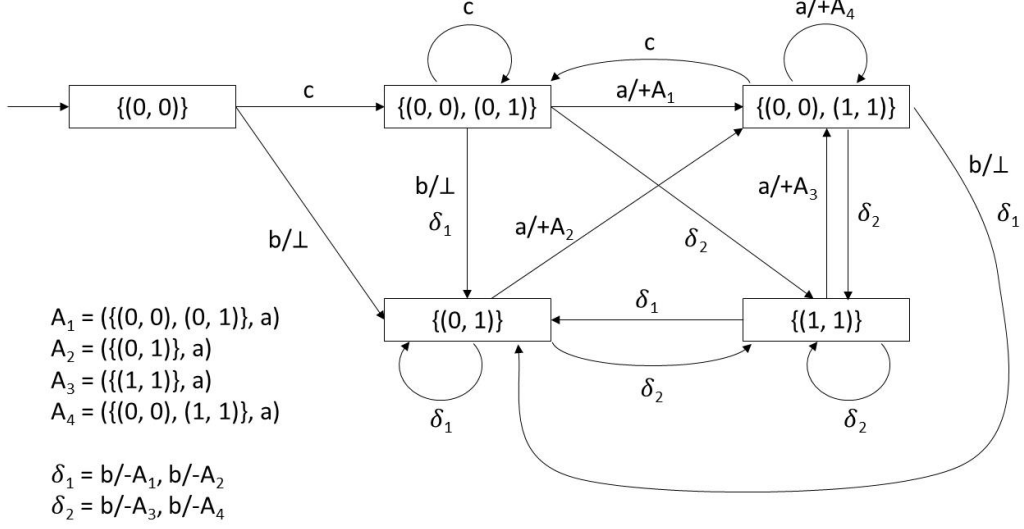
For a finite set  $X$ , let  $Id_X = \{(q, q) \mid q \in X\}$ . Let  $M = (Q, Q_0, \Gamma, F, \Delta)$  be any non-deterministic VPA. Let  $M^d$  denotes an equivalent deterministic VPA of  $M$  such that  $L(M) = L(M^d)$ . We will construct  $M^d$  as in Algorithm 1. Remark that from this point, every time we mention about determinization, it refers to this Algorithm 1.

**Theorem 1.** ([38]) *For any VPA  $M$  over  $\Sigma$ , there is a deterministic VPA  $M^d$  over  $\Sigma$  such that  $L(M^d) = L(M)$ . Moreover, if  $M$  has  $n$  states, we can construct  $M^d$  with at most  $2^{n^2}$  states and with stack symbols of size at most  $2^{n^2} \cdot |\Sigma_c|$ .*

**Example 1.** *We illustrate an example of determinization in Figure 2.1 where  $\Sigma_c = \{a\}$ ,  $\Sigma_r = \{b\}$  and  $\Sigma_i = \{c\}$ . The unreachable states are omitted, for example, state  $\{(1, 0)\}$  and  $\{(0, 1), (1, 0)\}$ . Note that in  $M^d$  some transitions actually are not possible but can be generated from the construction. For example, the transition  $\{(0, 0), (1, 1)\} \xrightarrow{b/\perp} \{(0, 1)\}$  would never happen because the configuration  $(\{(0, 0), (1, 1)\}, \perp)$  is not reachable.*



(a) A non-deterministic VPA  $M$



(b) A deterministic VPA  $M^d$  of  $M$

Figure 2.1: An example of determinization of VPA

## 2.2 $\mathcal{P}$ -automata

The purpose of  $\mathcal{P}$ -automaton is to keep track of reachable configurations of pushdown system. Bouajjani *et al.* [12] have introduced an efficient symbolic method to compute reachable configurations of a pushdown system. The key of their technique is to use a finite automaton so-called  $\mathcal{P}$ -automaton to encode a set of infinite configurations of a pushdown system.  $\mathcal{P}$ -automata are classified into  $Post^*$ -automata and  $Pre^*$ -automata, but we only use  $Post^*$ -automata. So, all  $\mathcal{P}$ -automata mentioned in this work are  $Post^*$ -automata.

Our definition of  $\mathcal{P}$ -automaton here has been adjusted so that the concepts are easily related to VPA, without disturbing the essential points in [12],

**Definition 3** ( $\mathcal{P}$ -automata). *For a VPA  $M = (Q, Q_0, \Gamma, F, \Delta)$  over  $\Sigma$ , a finite automaton  $\mathcal{A} = (P, \Gamma, P_0, \{f_{\mathcal{A}}\}, \nabla)$  is a  $\mathcal{P}$ -automaton for  $M$  if*

- $\mathcal{A}$  uses stack alphabet  $\Gamma$  as the input alphabet

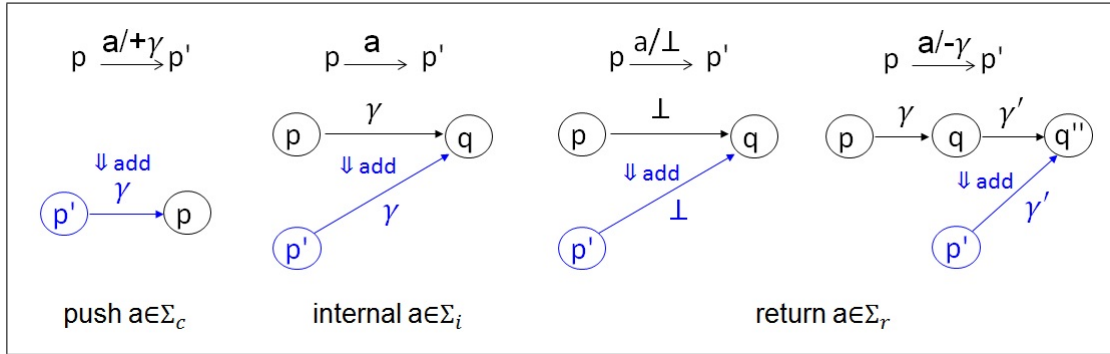
- $P \subseteq Q \cup \{f_A\}$  is a finite set of states
- $P_0 \subseteq Q$  is a set of (multiple) initial states
- the final state is only an unique state  $f_A$
- $\nabla \subseteq (Q \times (\Gamma \setminus \{\perp\}) \times Q) \cup (Q \times \{\perp\} \times \{f_A\})$  is the set of transitions

We write  $p \xrightarrow{\gamma} p'$  for  $(p, \gamma, p') \in \nabla$  and  $\mapsto^*$  for the reflexive transitive closure of  $\mapsto$ . A  $\mathcal{P}$ -automaton  $\mathcal{A}$  accepts a configuration  $(q, \sigma)$  if  $q \mapsto^* f_A$ . A set of configurations accepted by  $\mathcal{A}$  is denoted by  $Conf(\mathcal{A})$ .

**Definition 4.** Let  $\mathcal{A}$  and  $\mathcal{A}'$  be  $\mathcal{P}$ -automata of VPA  $M$ ,

1. we say  $\mathcal{A} \Rightarrow \mathcal{A}'$  if  $\mathcal{A}'$  is obtained from  $\mathcal{A}$  by one of the following saturation rules:

$$\begin{array}{ll}
[c] \frac{(P, \Gamma, P_0, \{f_A\}, \nabla)}{(P \cup \{q'\}, \Gamma \cup \{a\}, P_0 \cup \{q'\}, \{f_A\}, \nabla \cup (q', \gamma, q))} & \text{if } q \xrightarrow{a/+ \gamma} q' \in \Delta_c \\
[i] \frac{(P, \Gamma, P_0, \{f_A\}, \nabla)}{(P \cup \{q'\}, \Gamma, P_0 \cup \{q'\}, \{f_A\}, \nabla \cup (q', a', q''))} & \text{if } q \xrightarrow{a} q' \in \Delta_i \text{ and } q \xrightarrow{a'} q'' \in \nabla \\
[r_0] \frac{(P, \Gamma, P_0, \{f_A\}, \nabla)}{(P \cup \{q'\}, \Gamma, P_0 \cup \{q'\}, \{f_A\}, \nabla \cup (q', \perp, f_A))} & \text{if } q \xrightarrow{a/\perp} q' \in \Delta_r \text{ and } q \xrightarrow{\perp} f_A \in \nabla \\
[r] \frac{(P, \Gamma, P_0, \{f_A\}, \nabla)}{(P \cup \{q'\}, \Gamma, P_0 \cup \{q'\}, \{f_A\}, \nabla \cup (q', \gamma', q'''))} & \text{if } q \xrightarrow{a/- \gamma} q' \in \Delta_r \text{ and } q \xrightarrow{\gamma} q'' \xrightarrow{\gamma'} q''' \in \nabla
\end{array}$$



2. A  $\mathcal{P}$ -automaton  $\mathcal{A}$  is saturated if  $\mathcal{A} = \mathcal{A}'$  whenever  $\mathcal{A} \Rightarrow \mathcal{A}'$ .

Let  $\Rightarrow^*$  denotes the reflexive transitive closure of  $\Rightarrow$ . Remark that from definition above a saturated  $\mathcal{P}$ -automaton with  $\mathcal{A} \Rightarrow^* \mathcal{A}'$  is always uniquely determined and we denote it by  $Post^*(\mathcal{A})$ .

For VPA  $M = (Q, Q_0, \Gamma, F, \Delta)$ , let  $\mathcal{C}_0$  be a set of initial configurations of  $M$  (i.e.,  $\mathcal{C}_0 = \{(q_0, \perp) \mid q_0 \in Q_0\}$ ),  $\mathcal{A}_0$  denotes an initial  $\mathcal{P}$ -automaton that accepting all configurations in  $\mathcal{C}_0$ . Then  $\mathcal{A}_0 = (Q_0 \cup \{f_A\}, \Gamma, Q_0, \{f_A\}, \nabla_0)$  where  $\nabla_0 = \{(q_0, \perp, f_A) \mid q_0 \in Q_0\}$ .

Let  $post^*(\mathcal{C}_0)$  denotes all configurations reachable from  $\mathcal{C}_0$  that is set of all reachable configurations of  $M$  (i.e.,  $post^*(\mathcal{C}_0) = \{(q, \sigma) \mid \exists q_0 \in Q_0, \exists w \in \Sigma^*. (q_0, \perp) \xrightarrow{w}^* (q, \sigma)\}$ ). Given  $\mathcal{C}_0$ , we can compute  $post^*(\mathcal{C}_0)$  by constructing  $Post^*(\mathcal{A}_0)$  following Definition 4.

**Lemma 1.** *Let  $M = (Q, Q_0, \Gamma, F, \Delta)$  be a VPA, and let  $\mathcal{A}_0$  be a  $\mathcal{P}$ -automaton accepting  $\mathcal{C}_0$ . Assume that  $p \xrightarrow{\sigma}^* q$  in  $Post^*(\mathcal{A}_0)$  and  $p \in Q$ .*

1. *If  $q \in Q$ ,  $(q, \epsilon) \hookrightarrow^* (p, \sigma)$ .*
2. *If  $q = f_{\mathcal{A}}$ , there exists  $p' \xrightarrow{\sigma'}^* f_{\mathcal{A}}$  in  $\mathcal{A}_0$  and  $(p', \sigma') \hookrightarrow^* (p, \sigma)$ .*

*Proof.* Assume the saturation procedure proceeds  $\mathcal{A}_0, \mathcal{A}_1, \mathcal{A}_2, \dots$ . Then  $p \xrightarrow{\sigma}^* q$  is in  $\mathcal{A}_i$  for some  $i$ . We will prove by induction on steps of  $i$ . For  $i = 0$ ,  $\mathcal{A}_0 = (Q_0 \cup \{f_{\mathcal{A}}\}, \Gamma, Q_0, \{f_{\mathcal{A}}\}, \nabla_0)$  where  $\nabla_0 = \{(q_0, \perp, f_{\mathcal{A}}) \mid q_0 \in Q_0\}$ . Thus, only  $q = f_{\mathcal{A}}$  and the second statement holds with  $p' = p$  and  $\sigma' = \sigma$ . Assume the above statements hold for every  $p \xrightarrow{\sigma}^* q$  in  $\mathcal{A}_i$  and  $\mathcal{A}_{i+1}$  is constructed by adding new transitions (denoted by  $\mapsto_{i+1}$ ) by one of the saturation rules in Definition 4. We will prove for  $p \xrightarrow{\sigma}^* q$  in  $\mathcal{A}_{i+1}$ .

Assume that  $p \xrightarrow{\sigma}^* q$  contains  $\mapsto_{i+1}$   $k$ -times, we prove by (nested) induction on  $k$ . If  $k = 0$ , obviously the lemma holds from induction hypothesis. Let  $k > 0$  and the leftmost occurrence of  $\mapsto_{i+1}$  in  $p \xrightarrow{\sigma}^* q$  be  $p_0 \xrightarrow{\delta} p'_0$ . Thus,

$$p \xrightarrow{\sigma_1}^* p_0 \xrightarrow{\delta} p'_0 \xrightarrow{\sigma_2}^* q$$

where  $\sigma = \sigma_1 \delta \sigma_2$  and  $p'_0 \xrightarrow{\sigma_2}^* q$  contains  $\mapsto_{i+1}$  at most  $k - 1$  times.

We will prove only the first statement of the lemma. The second statement follows similarly. We have four cases according to which rule is applied when construct  $\mathcal{A}_i \Rightarrow \mathcal{A}_{i+1}$ .

1. Rule  $[c]$  is applied.
2. Rule  $[i]$  is applied.
3. Rule  $[r_0]$  is applied.
4. Rule  $[r]$  is applied.

By (both) induction hypothesis on  $p'_0 \xrightarrow{\sigma_2}^* q$  and  $p \xrightarrow{\sigma_1}^* p_0$ , we have  $(q, \epsilon) \hookrightarrow^* (p'_0, \sigma_2)$  and  $(p_0, \epsilon) \hookrightarrow^* (p, \sigma_1)$ , respectively.

**Case 1.** By the saturation rule  $[c]$ , we have  $(p'_0, \epsilon) \hookrightarrow (p_0, \delta)$ . Thus,

$$(q, \epsilon) \hookrightarrow^* (p'_0, \sigma_2) \hookrightarrow (p_0, \delta \sigma_2) \hookrightarrow^* (p, \sigma_1 \delta \sigma_2)$$

**Case 2.** By the saturation rule  $[i]$ , we have  $\exists p' \in Q$  that  $(p', \gamma) \hookrightarrow (p_0, \gamma)$  and  $p' \xrightarrow{\gamma} p'_0$  in  $\mathcal{A}_i$ . From induction hypothesis, we have  $(p'_0, \epsilon) \hookrightarrow^* (p', \gamma)$ . Thus,

$$(q, \epsilon) \hookrightarrow^* (p'_0, \sigma_2) \hookrightarrow^* (p', \gamma \sigma_2) \hookrightarrow (p_0, \gamma \sigma_2) \hookrightarrow^* (p, \sigma_1 \gamma \sigma_2)$$

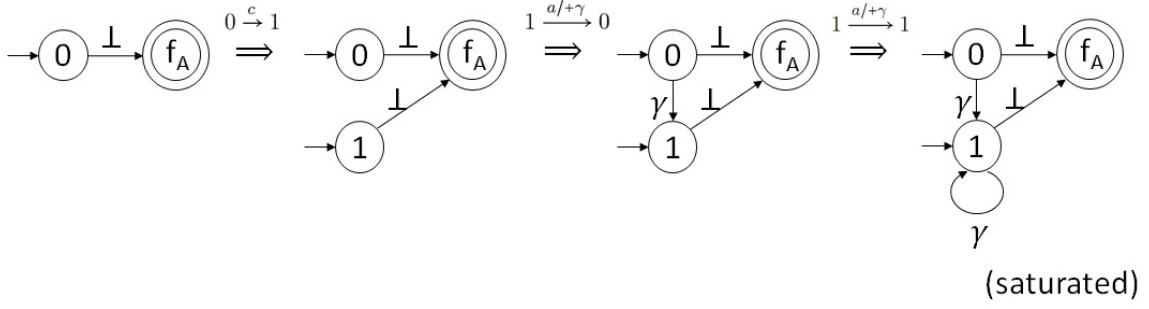


Figure 2.2: An example of  $\mathcal{P}$ -automaton construction

**Case 3.** By the saturation rule  $[r_0]$ , we have  $q = p'_0 = f_{\mathcal{A}}, \sigma' = \gamma = \perp, \sigma_2 = \epsilon$ . For rule  $[r_0]$ , the lemma always holds by the second statement.

**Case 4.** By the saturation rule  $[r]$ , we have  $\exists p', q' \in Q : (p', \gamma) \hookrightarrow (p_0, \epsilon)$ , and  $p' \xrightarrow{\gamma} q' \xrightarrow{\delta} p'_0$  in  $\mathcal{A}_i$ . From induction hypothesis, we have  $(p'_0, \epsilon) \hookrightarrow (q', \delta)$  and  $(q', \epsilon) \hookrightarrow (p', \gamma)$ . Thus,

$$(q, \epsilon) \hookrightarrow^* (p'_0, \sigma_2) \hookrightarrow^* (q', \delta\sigma_2) \hookrightarrow^* (p', \gamma\delta\sigma_2) \hookrightarrow (p_0, \delta\sigma_2) \hookrightarrow^* (p, \sigma_1\delta\sigma_2)$$

□

Lemma 1 shows that each accepted configuration of  $Post^*(\mathcal{A}_{\mathcal{C}})$  during the saturation process is in  $post^*(\mathcal{C})$  (*soundness*). On the other hand, saturation rules put immediate successor configurations, and all configurations in  $post^*(\mathcal{C})$  are finally accepted by  $Post^*(\mathcal{C})$  (*completeness*).

**Theorem 2.**  $post^*(\mathcal{C}_0) = Conf(Post^*(\mathcal{A}_0))$

**Example 2.** Figure 2.2 shows the example of  $\mathcal{P}$ -automaton construction of VPA  $M$  from Figure 2.1(a). However, it saturates before all the transition rules are applied. The initial configuration of  $M$  is:  $\mathcal{C}_0 = \{(0, \perp)\}$ , and the reachable configurations are:  $Post^*(\mathcal{C}_0) = \{(0, \gamma^*\perp), (1, \gamma^*\perp)\}$ .



# Chapter 3

## Antichain algorithm for universality checking

Recall that a non-deterministic VPA  $M = (Q, Q_0, \Gamma, F, \Delta)$ , and a determinized VPA  $M^d$  of  $M$  by Algorithm 1 (Section 2.1) is  $(Q', Q'_0, \Gamma', F', \Delta')$ .

### 3.1 Standard method

The standard algorithm for universality of VPA is to first determinize the automaton, and then check for the reachability of a non-accepting state (or non-accepting configuration). All reachable configurations of determinized VPA can be computed by using  $\mathcal{P}$ -automaton technique to compute  $Post^*(\mathcal{C}_0)$ . A configuration  $c = (q, w)$  is non-accepting (or rejecting) if  $q$  is not a final state. When a rejecting configuration is found, we stop and report that the original VPA is not universal. Otherwise, if all reachable configurations of determinized VPA are accepting, we report that the original VPA is universal.

**Definition 5.** For determinized VPA  $M^d$ , let  $ReachableConf(M^d)$  denotes the set of all reachable configurations of  $M^d$  and  $RejectingConf(M^d)$  denotes the set of rejecting configurations of  $M^d$ . Let  $q \in Q'$  and  $\sigma \in (\Gamma' \setminus \{\perp\})^* \cdot \perp$  be a state and stack of  $M^d$ , respectively.

- $ReachableConf(M^d) = \{(q, \sigma) \mid \exists w \in \Sigma^* : (Id_{Q_0}, \perp) \xrightarrow{w}^* (q, \sigma)\}$ .
- $RejectingConf(M^d) = \{(q, \sigma) \mid q \notin F'\}$ .

Recall that  $Id_{Q_0}$  is an only single initial state of  $M^d$  by Algorithm 1 (Section 2.1).

With the above algorithm and definition, we obtain the following theorem:

**Theorem 3.** *Let  $M$  be a non-deterministic VPA.  $M$  is not universal iff there exists a rejecting configuration of  $M^d$ , i.e.,  $\text{ReachableConf}(M^d) \cap \text{RejectingConf}(M^d) \neq \emptyset$ .*

## 3.2 Antichain algorithm

In this section, we present an antichain algorithm to check universality of VPAs [37]. An antichain algorithm is a combination between on-the-fly method and minimization. On-the-fly method is used for finding a rejecting configuration more efficiently than the standard method, and the minimization is used to reduce the number of computations based on a partial ordering over states and stack symbols of  $M^d$ .

### 3.2.1 On-the-fly method

In this subsection we propose an on-the-fly method to check universality of VPAs by performing determinization and  $\mathcal{P}$ -automaton construction simultaneously.

**Simultaneous on-the-fly determinization and  $\mathcal{P}$ -automaton construction.** To improve the efficiency, we perform simultaneously on-the-fly determinization and  $\mathcal{P}$ -automaton construction. There are two interleaving phases in this approach. First, we determinize VPA  $M$  step by step (iterations). After each step of determinization, we update the  $\mathcal{P}$ -automaton. Second, using new configurations obtained from newly added transitions of  $\mathcal{P}$ -automaton, perform the determinization again, and so on. It is crucial to note that this procedure terminates. This is because the size of  $M^d$  is finite, and the  $\mathcal{P}$ -automaton is finally terminated. From standard method and Theorem 3, checking universality of  $M$  is finding a rejecting configuration of  $M^d$ . In Algorithm 2, we present an on-the-fly way to explore such a rejecting configuration (if there is any).

**Example 3.** *We illustrate an example of on-the-fly algorithm in Figure 3.1 where  $\Sigma_c = \{a\}$ ,  $\Sigma_r = \{b\}$  and  $\Sigma_i = \{c\}$ .*

The processes of the algorithm is performed as below:

1. At the first time, assume that an initial state of  $M^d$  is state  $q_1$ . If  $q_1$  is rejecting, report that  $M$  is not universal immediately.
2. Then, the  $\mathcal{P}$ -automaton  $\mathcal{A}_{C_0}$  is constructed with two states  $\{q_1, f_A\}$  and only one transition  $q_1 \xrightarrow{\perp} f_A$ , where  $f_A$  is a unique final state. For this time,  $\mathcal{A}_{\text{Post}^*(C_0)}$  is representing a set of reachable configurations of  $M^d$   $\{(q_1, \perp)\}$ .

---

**Algorithm 2:** On-the-fly algorithm

---

**Data:** A non-deterministic VPA  $M = (Q, Q_0, \Gamma, F, \Delta)$

**Result:** Universality of  $M$

**begin**

    Create the initial state of determinized VPA  $M^d$

**if** *the initial state of  $M^d$  is not a final state* **then**

        | **return** *False*;

**end**

    Initiate  $\mathcal{P}$ -automaton  $\mathcal{A}_{C_0}$  to present the initial configuration of  $M^d$

$\mathcal{A}_{Post^*(C_0)} \leftarrow \mathcal{A}_{C_0}$

    Create transitions of  $M^d$  departing from the initial state

**while** *set of new transitions of  $M^d$  is not empty* **do**

        | Update  $\mathcal{A}_{Post^*(C_0)}$  using new transitions of  $M^d$

**if** *a rejecting state is added to  $\mathcal{A}_{Post^*(C_0)}$*  **then**

            | **return** *False*;

**end**

        | Update transitions of  $M^d$  using new configurations from  $\mathcal{A}_{Post^*(C_0)}$

**end**

**return** *True*;

**end**

---

3. Update  $M^d$  departing from  $(q_1, \perp)$ . Suppose  $M^d$  has new transitions  $\{q_1 \xrightarrow{a/(q_1,a)} q_2, q_1 \xrightarrow{b/\perp} q_3, q_1 \xrightarrow{c} q_4\}$ .
4. Update  $\mathcal{A}_{Post^*(C_0)}$  using new transitions of  $M^d$ . Now,  $\mathcal{A}_{Post^*(C_0)}$  is representing  $\{(q_1, \perp), (q_2, (q_1, a)\perp), (q_3, \perp), (q_4, \perp)\}$ . If one of  $q_2, q_3$  and  $q_4$  is rejecting, report that  $M$  is not universal.
5. We again update  $M^d$  using new configurations  $\{(q_2, (q_1, a)\perp), (q_3, \perp), (q_4, \perp)\}$  of  $\mathcal{A}_{Post^*(C_0)}$ , and so on.

### 3.2.2 Minimization

Recall that state  $Q'$  of  $M^d$  belongs to  $2^{Q \times Q}$  and stack symbols  $\Gamma'$  belongs to  $Q' \times \Sigma_c$ . We define an ordering over states and stack symbols as follows:

**Definition 6** (Partial ordering over states and stack symbols).

- Let  $S_1$  and  $S_2$  be states of  $M^d$ , we say  $S_1 \leq S_2$  if  $S_1 \subseteq S_2$ .

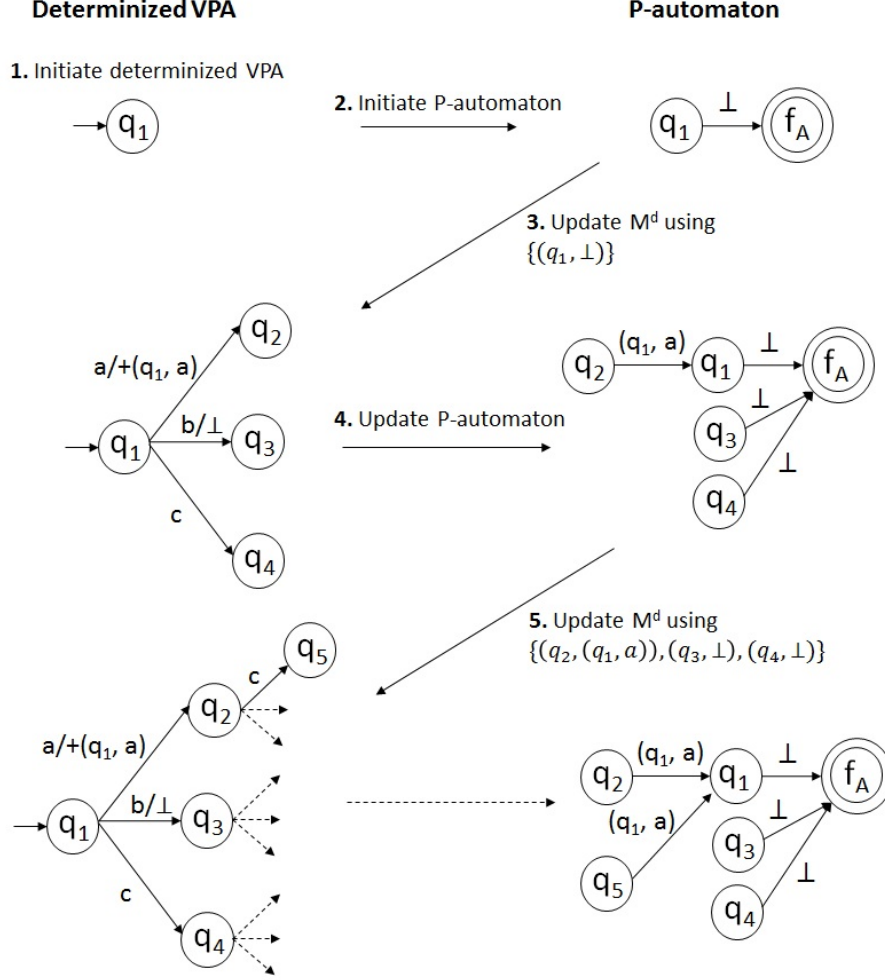


Figure 3.1: An example of on-the-fly algorithm

- Let  $a_1, a_2 \in \Sigma_c$  and let  $\gamma_1 = (S_1, a_1)$  and  $\gamma_2 = (S_2, a_2)$  be stack symbols of  $M^d$ , we say  $\gamma_1 \leq \gamma_2$  if  $S_1 \subseteq S_2$  and  $a_1 = a_2$ .

From Definition 6, we extend the ordering to an ordering over configurations of the determinized VPA  $M^d$ .

**Definition 7** (Partial ordering over configurations). Let  $c_1 = (S_1, \gamma_k \dots \gamma_1 \perp)$  and  $c_2 = (S_2, \gamma'_k \dots \gamma'_1 \perp)$  be configurations of  $M^d$ . We say  $c_1 \leq c_2$  if  $S_1 \leq S_2$  and  $\gamma_i \leq \gamma'_i$  for all  $1 \leq i \leq n$ .

Now we begin with establishing theoretical background for minimization in our algorithm.

**Lemma 2.** Let  $S_1 \xrightarrow{a/(S_1, a)} Id_{R'_1}$  and  $S_2 \xrightarrow{a/(S_2, a)} Id_{R'_2}$  be push transitions of  $M^d$ . If  $S_1 \leq S_2$ , then  $Id_{R'_1} \leq Id_{R'_2}$ .

*Proof.* By the determinization, we have:

- $S_1 \xrightarrow{a/+(S_1,a)} Id_{R'_1}$  where  $R'_1 = \{q' \mid \exists q \in \Pi_2(S_1) : q \xrightarrow{a/+\gamma} q' \in \Delta_c\}$
- $S_2 \xrightarrow{a/+(S_2,a)} Id_{R'_2}$  where  $R'_2 = \{q' \mid \exists q \in \Pi_2(S_2) : q \xrightarrow{a/+\gamma} q' \in \Delta_c\}$

Thus, if  $S_1 \leq S_2$  then  $R'_1 \leq R'_2$ , and obviously  $Id_{R'_1} \leq Id_{R'_2}$ .  $\square$

**Lemma 3.** Let  $S_1 \xrightarrow{a} S'_1$  and  $S_2 \xrightarrow{a} S'_2$  be internal transitions of  $M^d$ . If  $S_1 \leq S_2$ , then  $S'_1 \leq S'_2$ .

*Proof.* By the determinization, we have:

- $S_1 \xrightarrow{a} S'_1$  where  $S'_1 = \{(q, q') \mid \exists q'' \in Q : (q, q'') \in S_1, q'' \xrightarrow{a} q' \in \Delta_i\}$
- $S_2 \xrightarrow{a} S'_2$  where  $S'_2 = \{(q, q') \mid \exists q'' \in Q : (q, q'') \in S_2, q'' \xrightarrow{a} q' \in \Delta_i\}$

Thus, if  $S_1 \leq S_2$  then  $S'_1 \leq S'_2$ .  $\square$

**Lemma 4.** Let  $S_1 \xrightarrow{a/\perp} S'_1$  and  $S_2 \xrightarrow{a/\perp} S'_2$  be return transitions with empty stack of  $M^d$ . If  $S_1 \leq S_2$ , then  $S'_1 \leq S'_2$ .

*Proof.* By the determinization, we have:

- $S_1 \xrightarrow{a/\perp} S'_1$  where  $S'_1 = \{(q, q') \mid \exists q'' \in Q : (q, q'') \in S_1, q'' \xrightarrow{a/\perp} q' \in \Delta_r\}$
- $S_2 \xrightarrow{a/\perp} S'_2$  where  $S'_2 = \{(q, q') \mid \exists q'' \in Q : (q, q'') \in S_2, q'' \xrightarrow{a/\perp} q' \in \Delta_r\}$

Thus, if  $S_1 \leq S_2$  then  $S'_1 \leq S'_2$ .  $\square$

**Lemma 5.** Let  $S_1 \xrightarrow{a/-(S'_1,a'_1)} S''_1$  and  $S_2 \xrightarrow{a/-(S'_2,a'_2)} S''_2$  be return transitions with non-empty stack of  $M^d$ . If  $S_1 \leq S_2$  and  $(S'_1, a'_1) \leq (S'_2, a'_2)$ , then  $S''_1 \leq S''_2$ .

*Proof.* By the determinization, we have:

- $update_1 = \{(q, q') \mid \exists q_1, q_2 \in Q : (q_1, q_2) \in S_1, q_1 \xrightarrow{a'_1/+\gamma} q_1 \in \Delta_c, q_2 \xrightarrow{a'/-\gamma} q' \in \Delta_r\}$   
 $S''_1 = \{(q_3, q') \mid \exists q \in Q : (q_3, q) \in S'_1, (q, q') \in update_1\}$
- $update_2 = \{(q, q') \mid \exists q_1, q_2 \in Q : (q_1, q_2) \in S_2, q_1 \xrightarrow{a'_2/+\gamma} q_1 \in \Delta_c, q_2 \xrightarrow{a'/-\gamma} q' \in \Delta_r\}$   
 $S''_2 = \{(q_3, q') \mid \exists q \in Q : (q_3, q) \in S'_2, (q, q') \in update_2\}$

Thus, if  $S_1 \leq S_2$  and  $a'_1 = a'_2$ , then  $update_1 \subseteq update_2$ . And plus  $S'_1 \leq S'_2$ , then  $S''_1 \leq S''_2$ .  $\square$

Then, for configurations:

**Lemma 6.** Let  $c_1 = (S_1, \gamma_k \dots \gamma_1 \perp)$  and  $c_2 = (S_2, \gamma'_k \dots \gamma'_1 \perp)$  be configurations of  $M^d$  such that  $c_1 \leq c_2$ . For any word  $w \in \Sigma^*$ , if  $c_1 \xrightarrow{w} c'_1$  and  $c_2 \xrightarrow{w} c'_2$ , then  $c'_1 \leq c'_2$ .

*Proof.* We prove this lemma by induction on length  $|w|$  of  $w$ . For  $|w| = 1 = a$ , the lemma holds immediately from Lemma 2, 3, 4 or 5 with regarding to type of input symbol  $|a|$ . For

induction case, assume the lemma holds for  $w$ . We can prove for  $wa$  by using induction hypothesis, and Lemma 2, 3, 4 or 5 with regarding to type of  $a$ .  $\square$

**Lemma 7.** *Let  $c_1$  and  $c_2$  be configurations of  $M^d$ , If  $c_2$  is a rejecting configuration and  $c_1 \leq c_2$ , then  $c_1$  is also a rejecting configuration.*

*Proof.* Let  $c_1 = (S_1, \gamma_k \dots \gamma_1 \perp)$  and  $c_2 = (S_2, \gamma'_k \dots \gamma'_1 \perp)$  that  $c_1 \leq c_2$ . Recall that  $c_2$  is a rejecting configuration if  $S_2 \notin F'$  where  $F'$  is a set of final states of  $M^d$ . From Algorithm 1 (Section 2.1), that means  $\Pi_2(S_2) \cap F = \emptyset$ . Note that if  $c_1 \leq c_2$ , then  $S_1 \leq S_2$ . Thus,  $\Pi_2(S_2) \cap F = \emptyset$ .  $\square$

From Lemma 6 and 7, we can conclude that it is sufficient to compute only minimal reachable configurations to check for the existence of a rejecting configuration. Formally, we define:

**Definition 8.**  $MinimalReachableConf(M^d) = \{(s, \sigma) \in ReachableConf(M^d) \mid \neg \exists (s', \sigma') \in ReachableConf(M^d) : (s', \sigma') \leq (s, \sigma)\}$

**Theorem 4.** *Let  $M$  be a non-deterministic VPA.  $M$  is not universal iff there exists a rejecting **minimal** configuration of  $M^d$ , i.e.,  $MinimalReachableConf(M^d) \cap RejectingConf(M^d) \neq \emptyset$ .*

*Minimal  $\mathcal{P}$ -automaton.* Let  $\mathcal{C}_0 = \{(Id_{Q_0}, \perp)\}$  be the initial configuration of  $M^d$ . Let  $\mathcal{A}_{Post^*(\mathcal{C}_0)}$  be the  $\mathcal{P}$ -automaton presenting  $ReachableConf(M^d)$  and let  $\mathcal{A}_{Post^*(\mathcal{C}_0)}^{min}$  be the  $\mathcal{P}$ -automaton presenting  $MinimalReachableConf(M^d)$ .  $\mathcal{A}_{Post^*(\mathcal{C}_0)}^{min}$  is computed by minimizing the  $\mathcal{A}_{Post^*(\mathcal{C}_0)}$  as follows: “at each incremental expansion step” of constructing  $\mathcal{A}_{Post^*(\mathcal{C}_0)}$ , for two configurations  $(S_1, \gamma_1 \sigma)$  and  $(S_2, \gamma_2 \sigma)$ , we only need to compare the states (i.e.,  $S_1$  and  $S_2$ ) and top of stack (i.e.,  $\gamma_1$  and  $\gamma_2$ ). Assume that  $S_1 \leq S_2$  and  $\gamma_1 \leq \gamma_2$ , then  $(S_1, \gamma_1 \sigma) \leq (S_2, \gamma_2 \sigma)$ . So, we only need to keep the “smaller” configuration  $(S_1, \gamma_1 \sigma)$ . We formalize this observation in Algorithm 3.

Finally, an antichain algorithm which combines on-the-fly method and minimization is shown in Algorithm 4. Note that the difference is only in the while-loop, we extract  $\mathcal{P}$ -automaton to be minimal before continue determinizing VPA.

**Theorem 5** (Correctness of Algorithm 4). *Let  $M$  be a non-deterministic VPA. Let  $M_i^d$  and  $\mathcal{A}_i$  be determinized VPA and  $\mathcal{P}$ -automaton  $\mathcal{A}_{Post^*(\mathcal{C}_0)}^{min}$  obtained in the  $i$ -th iteration of the while-loop in Algorithm 4. The VPA  $M$  is not universal if and only if  $Conf(\mathcal{A}_i) \cap RejectingConf(M_i^d) \neq \emptyset$  for some  $i$ .*

*Proof.* If  $Conf(\mathcal{A}_i) \cap RejectingConf(M_i^d) \neq \emptyset$  for some  $i$  then of course  $M$  is not universal. Conversely, if  $M$  is not universal, there exists at least one rejecting reachable configuration  $(S, \sigma)$  (i.e.,  $\Pi_2(S) \cap F \neq \emptyset$ ). If  $(S, \sigma) \in Conf(\mathcal{A}_i)$  for some  $i$ , the theorem

---

**Algorithm 3:** Extract minimal transitions of  $\mathcal{P}$ -automaton

---

**Data:** A  $\mathcal{P}$ -automaton  $\mathcal{A} = (P, \Gamma, P_0, \{f_{\mathcal{A}}\}, \nabla)$ **Result:** A  $\mathcal{P}$ -automaton  $\mathcal{A}^{min} = (P, \Gamma, P_0, \{f_{\mathcal{A}}\}, \nabla')$ **begin**|  $\nabla' = \nabla$ | **for** each state  $S \in P$  **do**| | **if**  $(S_1, \gamma_1, S) \in \nabla' \wedge (S_2, \gamma_2, S) \in \nabla' \wedge S_1 \leq S_2 \wedge \gamma_1 \leq \gamma_2$  **then**| | |  $\nabla' \leftarrow \nabla' \setminus \{(S_2, \gamma_2, S)\}$ | | **end**| **end****end**

---

---

**Algorithm 4:** Antichain algorithm

---

**Data:** A non-deterministic VPA  $M = (Q, Q_0, \Gamma, F, \Delta)$ **Result:** Universality of  $M$ **begin**| Create the initial state of determinized VPA  $M^d$ | **if** the initial state of  $M^d$  is not a final state **then**| | **return** *False*;| **end**| Initiate  $\mathcal{P}$ -automaton  $\mathcal{A}_{C_0}$  to present the initial configuration of  $M^d$ |  $\mathcal{A}_{Post^*(C_0)}^{min} \leftarrow \mathcal{A}_{C_0}$ | Create transitions of  $M^d$  departing from the initial state| **while** set of new transitions of  $M^d$  is not empty **do**| | Update  $\mathcal{A}_{Post^*(C_0)}^{min}$  using new transitions of  $M^d$ | | **if** a rejecting state is added to  $\mathcal{A}_{Post^*(C_0)}^{min}$  **then**| | | **return** *False*;| | **end**| | Compute  $\mathcal{A}_{Post^*(C_0)}^{min}$  to be minimal by using Algorithm 3| | Update transitions of  $M^d$  using new configurations from  $\mathcal{A}_{Post^*(C_0)}^{min}$ | **end**| **return** *True*;**end**

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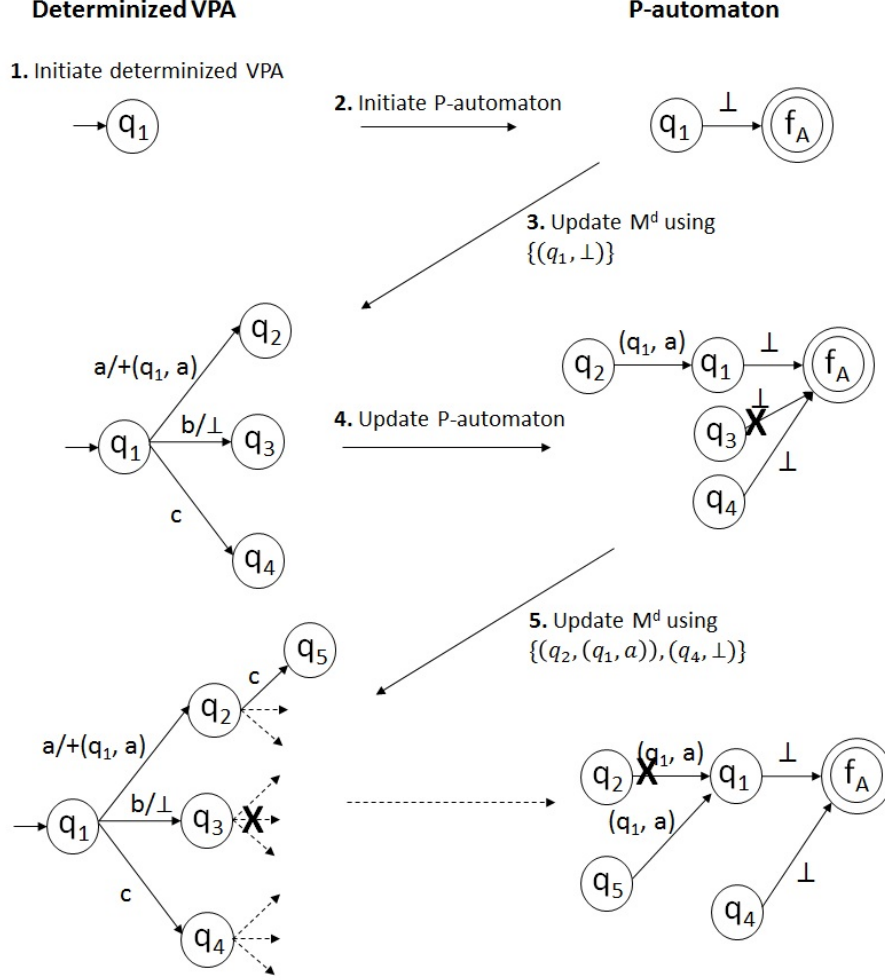


Figure 3.2: An example of antichain algorithm

holds. If  $(S, \sigma) \notin \text{Conf}(\mathcal{A}_i)$  for any  $i$  (within a bound), it means that the configuration  $(S', \sigma')$  that  $(S', \sigma') \xrightarrow{w^*} (S, \sigma)$  for some  $w \in \Sigma^*$  was taken out from  $\text{Conf}(\mathcal{A}_j)$  for  $j \leq i$  by Algorithm 3. Thus there is at least one reachable configuration  $(S'_2, \sigma'_2) \in \text{Conf}(\mathcal{A}_j)$  such that  $(S'_2, \sigma'_2) \leq (S', \sigma')$  and  $(S'_2, \sigma'_2) \xrightarrow{w^*} (S_2, \sigma_2) \in \text{Conf}(\mathcal{A}_i)$ . From Lemma 6 and 7, we have  $(S_2, \sigma_2) \leq (S, \sigma)$  and  $(S_2, \sigma_2)$  is a rejecting configuration. As a result, there exists an  $i$  such that  $\text{Conf}(\mathcal{A}_i) \cap \text{RejectingConf}(M_i^d) \neq \emptyset$ , the theorem holds.  $\square$

**Example 4.** To describe how antichain algorithm work, we revisit Example 3. Assume that in determinized VPA  $M^d$   $q_1 \leq q_3$  and  $q_5 \leq q_2$ . With these assumption, after step 4 of updating P-automaton, we remove state and transition of  $q_3$ . Plus, we do not need to investigate farther from  $q_3$ . And also after  $q_5$  appears, we can remove  $q_2$ . The illustration is shown in Figure 3.2.

*Complexity.* In the worst case (i.e., when the VPA is universal), the complexity of antichain-base algorithm is the same as the standard method, that is  $O(2^{3|Q|^2})$  where



$|Q|$  is a number of state of  $M$  (this is because checking emptiness of pushdown system is cubic time of number of state [12]). However, in the case of not universal VPA, the antichain algorithm really outperforms the standard one.

# Chapter 4

## Bounded model checking for VPA

Bounded model checking (BMC) is a SAT-based technique for symbolic model checking. The main idea of BMC is to avoid the full state space generation and look for a violation of a given property of bounded length. If no violation is found in that length, enlarge the bound and repeat the search again, until either a violation is found, or we can guarantee that no violation is lying beyond that length.

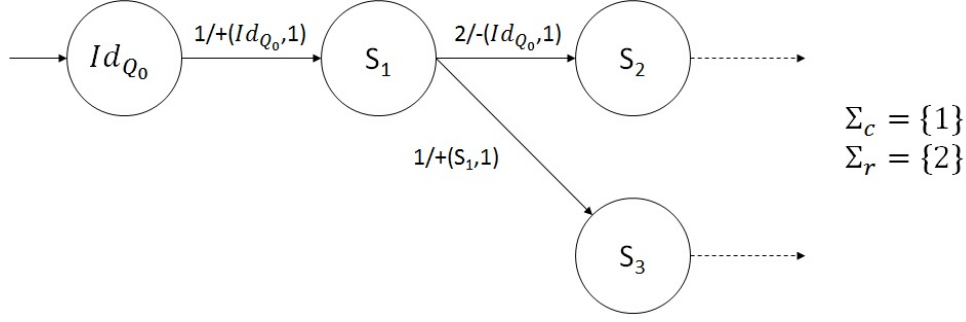
The basic idea of BMC for universality checking of VPA is to unroll the determinized VPA  $M^d$  for a fixed number of steps  $k$ , and check for universality of that  $k$ -step  $M^d$  (denoted by  $M_k^d$ ). If  $M_k^d$  is not universal, we can conclude that  $M^d$  is also not universal. Otherwise repeat the process with larger bound until  $M_k^d = M^d$  or we can guarantee that  $M^d$  is universal.

The question is how to unroll  $M^d$  to be  $M_k^d$ , what of  $M^d$  should be represented in  $M_k^d$  in order to decide universality, and how to obtain  $M_{k+1}^d$  from  $M_k^d$ .

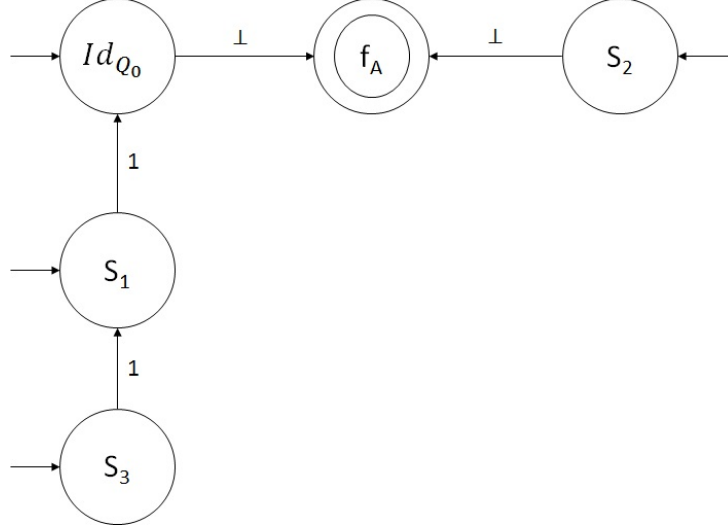
We have considered many options to bound  $M^d$ . Each step in bound  $k$  can be one step of applying:

1. transition  $\rightarrow$  of  $M^d$
2. transition  $\mapsto$  of  $\mathcal{P}$ -automaton of  $M^d$
3. transition  $\leftrightarrow$  of configurations
4. transition  $\rightarrow$  of  $\mathcal{P}$ -automaton transition (will be defined later)
5. saturation rule  $\Rightarrow$  of  $\mathcal{P}$ -automaton saturation process

In this chapter, we will explain each option and clarify a possibility of each option to be used for BMC for universality checking. Note that our BMC algorithm BMCi is based on the option 4 and BMCp is based on the option 5.



(a)  $M^d$



(b)  $\mathcal{P}$ -automaton of  $M^d$

Figure 4.1: An example of  $M^d$

## 4.1 Bounded transitions of $M^d$ ( $\rightarrow$ )

This is the simplest way to unroll  $M^d$ . Like BMC of finite automata,  $M_k^d$  represents  $M^d$  by reachable states in  $k$  steps. Starting from  $k = 0$ , the starting point is an initial state of  $M^d$   $Id_{Q_0}$ . Each step within bound is a transition  $\rightarrow$  of  $\Delta'$ .

**Example 5.** From determinized VPA  $M^d$  in Figure 4.1(a),

$$M_0^d = \{Id_{Q_0}\}, M_1^d = \{S_1\}, M_2^d = \{S_2, S_3\}$$

However, unlike finite automata, this bounded method does not work because of at least 2 reasons. (i)  $M_k^d$  does not contain any information about stack. From  $M_k^d$  we can compute  $M_{k+1}^d$  with call and internal transitions but not return transitions. For instance, in Example 5 actually we cannot know whether from  $M_1^d$  we will have  $S_2$  in  $M_2^d$  because we do not know whether stack is  $(Id_{Q_0}, 1)$ . (ii) To be able to compute  $M_{k+1}^d$  from length  $M_k^d$ , it requires a full set of transitions. That means we need to compute for a full  $\Delta'$

of  $M^d$ . If we do that successfully, we can just use  $\mathcal{P}$ -automaton to check reachability of rejecting configurations like in standard algorithm. It does not make sense to use BMC in the first place.

## 4.2 Bounded transitions of $\mathcal{P}$ -automaton ( $\mapsto$ )

Recall that the purpose  $\mathcal{P}$ -automaton is to encode configurations of  $M^d$  into a regular expression. Each configuration  $(q, \sigma)$  is accepted by  $\mathcal{P}$ -automaton if  $q \xrightarrow{\sigma^*} f_A$ . In this method, each step within bound is a *backward* of transition  $\mapsto$  of  $\nabla$ . Starting point  $M_0^d$  is  $f_A$ . For each  $k$ ,  $M_k^d$  represents states that can reach to  $f_A$  in  $k$  steps of  $\mathcal{P}$ -automaton. That are states that can reach to stack height  $k$  in  $M^d$ .

**Example 6.** From  $\mathcal{P}$ -automaton of  $M^d$  in Figure 4.1(b),

$$M_0^d = \{f_A\}, M_1^d = \{Id_{Q_0}, S_2\}, M_2^d = \{S_1\}$$

However, like finite automata, it requires a full set of transitions of  $\mathcal{P}$ -automaton to perform transitions like that. Again, it requires a full determinization of  $M^d$  to construct transitions of  $\mathcal{P}$ -automaton.

## 4.3 Bounded transitions of configuration ( $\leftrightarrow$ )

To keep information of states and stacks, we use  $M_k^d$  to represent  $M^d$  by reachable configurations instead of reachable states. Starting point is an initial configuration of  $M^d$  ( $Id_{Q_0}, \perp$ ). Each step within bound is a transition  $\leftrightarrow$  according to  $\Delta'$ .

**Example 7.** From determinized VPA  $M^d$  in Figure 4.1(a),

$$M_0^d = \{(Id_{Q_0}, \perp)\}, M_1^d = \{(S_1, (Id_{Q_0}, 1)\perp)\}, M_2^d = \{(S_2, \perp), (S_3, (S_1, 1)(Id_{Q_0}, 1)\perp)\}$$

This approach does not require a full set of transitions  $\Delta'$  like the previous two approaches. This is because if we do know current state and top of stack we can compute next state using determinization algorithm (Algorithm 1 Section 2.1), and can compute stack based on input alphabet. For instance, in Example 7  $M_1^d = \{(S_1, (Id_{Q_0}, 1)\perp)\}$ , we can compute for  $S_2$  using  $S_1$  and  $(Id_{Q_0}, 1)$  follow Algorithm 1, and pop top of stack out. On the other hand, we can compute  $S_3$  using  $S_1$  (ignore stack), and push  $(S_1, 1)$  to stack.

However, even if it is possible to unroll  $M^d$  without full computing determinization, this approach is not considered to be used for checking universality of VPAs. Because

configurations of  $M^d$  have an uncertain length, stack of  $M^d$  can be finitely any height. It is difficult to encode to a propositional formula.

## 4.4 Bounded transitions of $\mathcal{P}$ -automaton transition $(\rightarrow)$

Before we talk about transitions of  $\mathcal{P}$ -automaton transition  $(\rightarrow)$ , we have a crucial observation of  $\mathcal{P}$ -automaton of determinized VPA.

*Remark.* From the Definition 3 (Section 2.2) alphabet of  $\mathcal{P}$ -automaton is equal to the stack alphabet of VPA. The alphabet of  $\mathcal{P}$ -automaton of  $M^d$  must belong to  $Q' \times \Sigma_c$ . However, it is a crucial observation that every transition of  $\mathcal{P}$ -automaton, which does not lead to the final state (i.e.,  $p \xrightarrow{\gamma} p' \in Q'$ ), the first component of input symbol is always equal to the destination state (i.e.,  $\exists a' \in \Sigma_c : \gamma = (p', a')$ ). This is because of behaviors of the determinization process (Algorithm 1 Section 2.1) and  $\mathcal{P}$ -automaton saturation rules (Definition 4 Section 2.2). Therefore, we can cut out the alphabet of  $\mathcal{P}$ -automaton of  $M^d$  from  $Q' \times \Sigma_c$  to just  $\Sigma_c$ . For instance, as shown in Figure 4.1(b), actually the transitions must be  $S_1 \xrightarrow{(Id_{Q_0}, 1)} Id_{Q_0}$  and  $S_3 \xrightarrow{(S_1, 1)} S_1$

We define transitions  $\rightarrow$  between transitions of  $\mathcal{P}$ -automaton of  $M^d$  as follow:

**Definition 9** (transitions between  $\mathcal{P}$ -automaton transitions on  $\Sigma^*$ ). *For determinized VPA  $M^d = (Q', Q'_0, \Gamma', F', \Delta')$  and  $\mathcal{P}$ -automaton  $\mathcal{A} = (P, \Sigma_c, P_0, \{f_{\mathcal{A}}\}, \nabla)$  accepting configurations of  $M^d$ , a set  $\nabla'$  of transitions of  $\mathcal{P}$ -automaton transition is given by:*

- [c] *Call transition: For every  $a \in \Sigma_c$ ,  $(S \xrightarrow{a'} S'') \xrightarrow{a} (Id_{R'} \xrightarrow{a} S) \in \nabla'$  if  $S \xrightarrow{a/(S,a)} Id_{R'} \in \Delta'_c$*
- [i] *Internal transition: For every  $a \in \Sigma_i$ ,  $(S \xrightarrow{a'} S'') \xrightarrow{a} (S' \xrightarrow{a'} S''') \in \nabla'$  if  $S \xrightarrow{a} S' \in \Delta'_i$*
- [r<sub>0</sub>] *Return transition with empty stack: For every  $a \in \Sigma_r$ ,  $(S \xrightarrow{\perp} f_{\mathcal{A}}) \xrightarrow{a} (S' \xrightarrow{\perp} f_{\mathcal{A}}) \in \nabla'$  if  $S \xrightarrow{a/\perp} S' \in \Delta'_r$*
- [r] *Return transition: For every  $a \in \Sigma_r$ ,  $(S \xrightarrow{a'} S'') \xrightarrow{a} (S' \xrightarrow{a''} S''') \in \nabla'$  if  $S \xrightarrow{a/(S'', a')} S' \in \Delta'_r$  and  $S \xrightarrow{a'} S'' \xrightarrow{a''} S''' \in \nabla$*

We write  $\rightarrow^*$  for the reflexive transitive closure of  $\rightarrow$ .

The starting point is an initial transition of  $\mathcal{P}$ -automaton that is  $Id_{Q_0} \xrightarrow{\perp} f_{\mathcal{A}}$ . For each  $k$ ,  $M_k^d$  represents  $\mathcal{P}$ -automaton transitions after reading input of length  $k$ .

**Example 8.** From Figure 4.1, the set of transitions of  $\mathcal{P}$ -automaton transition  $\nabla'$  is:

$$\begin{aligned}\nabla' = & \{(Id_{Q_0} \xrightarrow{\perp} f_A) \xrightarrow{\perp} (S_1 \xrightarrow{\perp} Id_{Q_0}), \\ & (S_1 \xrightarrow{\perp} Id_{Q_0}) \xrightarrow{\perp} (S_2 \xrightarrow{\perp} f_A), \\ & (S_1 \xrightarrow{\perp} Id_{Q_0}) \xrightarrow{\perp} (S_3 \xrightarrow{\perp} S_1)\}\end{aligned}$$

and steps of unrolling  $M^d$  are:

$$\begin{aligned}M_0^d &= \{Id_{Q_0} \xrightarrow{\perp} f_A\}, M_1^d = \{S_1 \xrightarrow{\perp} Id_{Q_0}\}, \\ M_2^d &= \{S_2 \xrightarrow{\perp} f_A, S_3 \xrightarrow{\perp} S_1\}\end{aligned}$$

This method does not require a full determinization of  $M$ . Because for each  $M_k^d$  we can compute  $M_{k+1}^d$  simultaneously with determinization and  $\nabla'$  construction (Similar to on-the-fly algorithm in Algorithm 2 Section 3.2.1 but construct  $\nabla'$  instead of  $\mathcal{P}$ -automaton).

We can check universality of  $M_k^d$  by checking whether  $M_k^d$  contains a transition  $p \xrightarrow{a} p'$  that  $p \notin F'$ . We will explain in the next chapter why tracking  $\mathcal{P}$ -automaton transitions can check universality of VPA.

One of our BMC algorithms called BMCi is developed based on this approach.

## 4.5 Bounded $\mathcal{P}$ -automaton saturation process

( $\Rightarrow$ )

Let  $\mathcal{C}_0$  be an initial configuration of  $M^d$  and let  $\mathcal{A}_0$  be a  $\mathcal{P}$ -automaton accepting  $\mathcal{C}_0$ , Assume that the saturation procedure to compute  $Post^*(\mathcal{A}_0)$  proceeds  $\mathcal{A}_0 \Rightarrow \mathcal{A}_1 \Rightarrow \mathcal{A}_2 \dots$ . For each bound  $k$ ,  $M_k^d$  represents  $\mathcal{P}$ -automaton of  $M^d$  after applying saturation rules  $k$  times.

**Example 9.** From Figure 4.1,

$$\begin{aligned}M_0^d &= \{Id_{Q_0} \xrightarrow{\perp} f_A\}, \\ M_1^d &= \{Id_{Q_0} \xrightarrow{\perp} f_A, S_1 \xrightarrow{\perp} S_0\}, \\ M_2^d &= \{Id_{Q_0} \xrightarrow{\perp} f_A, S_1 \xrightarrow{\perp} S_0, S_2 \xrightarrow{\perp} f_A, S_3 \xrightarrow{\perp} S_1\}\end{aligned}$$

Note that this is different from the previous one. In bounded  $\rightarrow$ ,  $M_k^d$  represents  $\mathcal{P}$ -automaton transitions after reading input of length  $k$ . In bounded  $\Rightarrow$ ,  $M_k^d$  represents  $\mathcal{A}_k$ , a  $\mathcal{P}$ -automaton that obtained by applying saturation rules  $k$  times.

This method does not require a full determinization of  $M$ . Because we can perform determinization and  $\mathcal{P}$ -automaton construction simultaneously using on-the-fly algorithm (Algorithm 2 Section 3.2.1).

We can check universality of  $M_k^d$  by checking whether  $M_k^d (= \mathcal{A}_k)$  accepts any rejecting configurations, i.e., checking whether there exists a transition  $p \xrightarrow{a} p'$  that  $p \notin F'$  in  $M_k^d$ .

One of our BMC algorithms called BMCp is developed based on this approach.

# Chapter 5

## BMC and antichains for universality checking of VPA

In the previous chapter, we talked about how to unroll  $M^d$  for each bound  $k$ . In this chapter, we will talk about our two BMC algorithms, called BMCi and BMCp. BMCi unrolls  $M^d$  based on transitions of  $\mathcal{P}$ -automaton transition ( $\rightarrow$ ), and BMCp unrolls  $M^d$  based on  $\mathcal{P}$ -automaton saturation process ( $\Rightarrow$ ).

First of all, we will give an idea of how to check non-universality of VPAs (finding counter-examples) using BMC, and how to apply antichain algorithm to BMC for checking non-universality. Next we will explain our algorithms BMCi and BMCp and show differences of them in non-universality checking. Then we will talk about universality checking, and finally how to apply antichain to universality checking.

### 5.1 Encoding for non-universality checking of VPAs

In this section, we will first talk about how  $\mathcal{P}$ -automaton transitions are used to check non-universality. Next we will state the method for encoding transitions of  $\mathcal{P}$ -automaton transition ( $\rightarrow$ ) and non-universality checking to be propositional formulae.

#### 5.1.1 Basic idea

We first clarify why transitions of  $\mathcal{P}$ -automaton transition ( $\rightarrow$ ) relate to non-universality checking. We start with the relation between configurations of  $M^d$  and  $\mathcal{P}$ -automaton transitions. Recall that from the remark mentioned in Section 4.4, the input alphabet of  $\mathcal{P}$ -automaton of  $M^d$  is  $\Sigma_c$  rather than  $\Gamma'$ ,



**Definition 10.** Let  $\mathcal{A} = (P, \Sigma_c, P_0, \{f_{\mathcal{A}}\}, \nabla)$  be a  $\mathcal{P}$ -automaton of a determinized VPA  $M^d = (Q', Q'_0, \Gamma', F', \Delta')$ , and let  $\sigma = (q_n, a_n) \dots (q_1, a_1) \perp$  be a stack of  $M^d$  where  $q_i \in Q'$  and  $a_i \in \Sigma_c$ . For a configuration of  $M^d$   $c = (q, \sigma)$ , we define  $\delta_c = q \xrightarrow{a_n} q'$  as the first transition of accepting path of  $c$  in  $\mathcal{A}$  (i.e.,  $q \xrightarrow{a_n} q' \xrightarrow{a_{n-1}} \dots \xrightarrow{\perp} f_{\mathcal{A}}$ ).

**Definition 11.** For any word  $w \in \Sigma^*$ , let  $w = a_1 \dots a_k$  and let  $\delta_i$  are  $\mathcal{P}$ -automaton transitions for  $i \geq 0$ .

- A run of  $M^d$  on  $w$  is a sequence of configurations of  $M^d$  on  $w$  starting from an (unique) initial configuration, i.e.,  $(q_0, \perp) \xrightarrow{a_1} (q_1, \sigma_1) \dots \xrightarrow{a_k} (q_k, \sigma_k)$  where  $q_0 = Id_{Q_0}$ , and is denoted by  $(q_0, \perp) \xrightarrow{w}^* (q_k, \sigma_k)$ .
- A run of  $\delta$  on  $w$  is a sequence of  $\mathcal{P}$ -automaton transitions on  $w$  obtained by Algorithm 9 (Section 4.4), starting from an (unique) initial  $\mathcal{P}$ -automaton transition, i.e.,  $\delta_0 \xrightarrow{a_1} \delta_1 \dots \xrightarrow{a_k} \delta_k$  where  $\delta_0 = q_0 \xrightarrow{\perp} f_{\mathcal{A}}$ , and is denoted by  $\delta_0 \xrightarrow{w}^* \delta_k$ .

**Lemma 8.** Let  $\delta_i = q_i \xrightarrow{a_i} q'_i \in \nabla$  be transitions of  $\mathcal{P}$ -automaton of  $M^d$ . For input word  $w \in \Sigma^*$ , let a run of  $M^d$  on  $w$  be  $(q_0, \perp) \xrightarrow{w}^* (q_k, \sigma_k)$  and let  $\delta_0 \xrightarrow{w}^* \delta_k$  be a run of  $\delta$  on  $w$ . If  $\delta_0 = \delta_{(q_0, \perp)}$ , then  $\delta_k = \delta_{(q_k, \sigma_k)}$ .

*Proof.* We prove it by induction on length of  $w$ . For  $|w| = 0$ , the statement holds immediately. For induction case, assume above statement hold for  $|w| = k$ , and for  $wa_{k+1}$  a run of  $M^d$  is extended to  $(q_0, \perp) \xrightarrow{w}^* (q_k, \sigma_k) \xrightarrow{a_{k+1}} (q_{k+1}, \sigma_{k+1})$ , and a run of  $\delta$  is extended to  $\delta_0 \xrightarrow{w}^* \delta_k \xrightarrow{a_{k+1}} \delta_{k+1}$ . The proof of  $wa_{k+1}$  is based on the case of transition of  $a_{k+1}$ .

- $a_{k+1} \in \Sigma_c$ . Following behaviors of VPA and determinization, we have  $(q_k, \sigma_k) \xrightarrow{a_{k+1}} (Id_{q_k}, (q_k, a_{k+1})\sigma_k)$ . Following definition 9 (Section 4.4) [c],  $\delta_{k+1} = Id_{q_k} \xrightarrow{a_{k+1}} q_k$ , and from induction hypothesis,  $q_k \xrightarrow{\sigma_k}^* f_{\mathcal{A}}$ . Thus,  $\delta_{(q_{k+1}, \sigma_{k+1})} = Id_{q_k} \xrightarrow{a_{k+1}} q_k = \delta_{k+1}$ .
- $a_{k+1} \in \Sigma_i$ . Following behaviors of VPA and determinization, we have  $(q_k, \sigma_k) \xrightarrow{a_{k+1}} (q_{k+1}, \sigma_k)$ . Following definition 9 [i],  $\delta_{k+1} = q_{k+1} \xrightarrow{a_k} q'_k$ , and from induction hypothesis,  $q_k \xrightarrow{\sigma_k}^* f_{\mathcal{A}}$  with the first transition  $q_k \xrightarrow{a_k} q'_k$ . Thus,  $\delta_{k+1}$  is the first transition of  $q_{k+1} \xrightarrow{\sigma_k}^* f_{\mathcal{A}}$ , then  $\delta_{k+1} = \delta_{(q_{k+1}, \sigma_k)}$ .
- $a_{k+1} \in \Sigma_r$  and stack is empty. Following behaviors of VPA and determinization, we have  $(q_k, \perp) \xrightarrow{a_{k+1}} (q_{k+1}, \perp)$ . Following definition 9 [r<sub>0</sub>],  $\delta_{k+1} = q_{k+1} \xrightarrow{\perp} f_{\mathcal{A}}$ . Thus, obviously,  $\delta_{k+1} = \delta_{(q_{k+1}, \perp)}$ .
- $a_{k+1} \in \Sigma_r$  and stack is not empty. Following behaviors of VPA and determinization, we have  $(q_k, (q'', a')\sigma'_k) \xrightarrow{a_{k+1}} (q_{k+1}, \sigma'_k)$ . Following definition 9 [r],  $\delta_{k+1} = q_{k+1} \xrightarrow{a''} q'_{k+1}$  where  $q_k \xrightarrow{a'} q'' \xrightarrow{a''} q'_{k+1}$ , and from induction hypothesis, there must be  $i < k$  that  $q_i = q''$  and  $q_i \xrightarrow{\sigma_i}^* f_{\mathcal{A}}$ . Thus,  $\delta_{k+1} = \delta_{(q_{k+1}, \sigma'_k)}$  □

We say a configuration  $c = (q, \sigma)$  of  $M^d$  is rejecting if  $q \notin F'$ , and we say a  $\mathcal{P}$ -automaton transition  $\delta = p \xrightarrow{a} p'$  is rejecting if  $p \notin F'$ .

**Lemma 9.** *A configuration  $c$  of  $M^d$  is rejecting iff  $\delta_c$  is rejecting*

*Proof.* Let  $c = (q, \sigma)$ , from Definition 10  $\delta_c = q \xrightarrow{a} q'$ , it is obvious that  $c$  is rejecting if and only if  $\delta_c$  is rejecting  $\square$

From Lemma 8 and 9, suppose a run of  $M^d$  on  $w$  is  $(q_0, \perp) \xrightarrow{w}^* (q_k, \sigma_k)$  and suppose a run of  $\delta$  on  $w$  is  $\delta_0 \xrightarrow{w}^* \delta_k$ . If  $w \notin L(M^d)$ , then  $(q_k, \sigma_k)$  is rejecting, and also  $\delta_k$  is rejecting. We can conclude that we can use a run of  $\delta$  to monitor a run of  $M^d$  for non-universality checking. This is important because a configuration of  $M^d$  belongs to  $Q' \times ((\Gamma' \setminus \{\perp\})^* \cdot \perp)$  that have an uncertain length but  $\delta$  belongs to  $Q' \times \Sigma_c \times (Q' \cup \{f_{\mathcal{A}}\})$  that always has a certain length. Therefore, encoding of  $\delta$  ( $\mathcal{P}$ -automaton transitions) is a lot easier than encoding configurations directly.

**Theorem 6.**  *$M^d$  is not universal iff there exists  $k \in \mathbb{N}$  that  $w \in \Sigma^k$  and a run of  $\delta$  on  $w$  is  $\delta_0 \xrightarrow{w}^* \delta_k$  and  $\delta_k$  is rejecting*

### 5.1.2 Encoding of $\mathcal{P}$ -automaton transition

Each  $\mathcal{P}$ -automaton transition  $\delta = q \xrightarrow{a} q'$  is encoded to propositional variables as follows:

**Definition 12.** *Let  $n = |Q|$  and  $m = \lfloor \log(|\Sigma_c|) \rfloor + 1$ . We define three vectors of propositional variables:*

- $\mathbf{a} = (\mathbf{a}_{1,1}, \dots, \mathbf{a}_{1,n}, \mathbf{a}_{2,1}, \dots, \mathbf{a}_{n,n})$
- $\mathbf{b} = (\mathbf{b}_m, \dots, \mathbf{b}_1)$
- $\mathbf{c} = (\mathbf{c}_f, \mathbf{c}_{1,1}, \dots, \mathbf{c}_{1,n}, \mathbf{c}_{2,1}, \dots, \mathbf{c}_{n,n})$

*to represent each transition  $q \xrightarrow{a} q'$  of  $\mathcal{P}$ -automaton of  $M^d$ .*

*In particular, vector  $\mathbf{a}$  represents a source state of transition (i.e.,  $q$ ), vector  $\mathbf{b}$  represents an alphabet of transition (i.e.,  $a$ ), and vector  $\mathbf{c}$  represents a destination state of transition (i.e.,  $q'$ ).*

*Length.* Since states of  $\mathcal{P}$ -automaton of  $M^d$  belong to  $Q' \cup \{f_{\mathcal{A}}\}$ , size of states  $|P| = 2^{n^2} + 1$ . But from the definition 3 (Section 2.2) there is always no transition from the final state  $f_{\mathcal{A}}$ , length  $n^2$  is enough for vector  $\mathbf{a}$  but length of  $\mathbf{c}$  must be  $n^2 + 1$ . For vector  $\mathbf{b}$ , length  $m = \lfloor \log(|\Sigma_c|) \rfloor + 1$  is enough to encode the call alphabet.

*Intention.* Because  $|Q| = n$ , one can easily encode the set of states  $Q = \{1, \dots, n\}$ . Each state  $q$  of  $M^d$  is a set of pair of integers (i.e.,  $q \subseteq Q \times Q$ ). A vector  $\mathbf{a}$  is a bit representation of set  $q$ . Each propositional variable  $\mathbf{a}_{i,j}$  of vector  $\mathbf{a}$  ( $1 \leq i, j \leq n$ ) represents whether a pair  $(i, j)$  is in  $q$ , i.e.,  $\mathbf{a}_{i,j}$  is true denotes  $(i, j) \in q$  and false otherwise. Similarly,  $\mathbf{c}_{i,j}$  represents whether a pair  $(i, j)$  is in  $q'$ . An additional variable  $\mathbf{c}_f$  is used to represent whether the destination of transition is a final state  $f_A$ . Either  $q' \in Q'$  then  $\mathbf{c}_f$  is false and  $\mathbf{c}_{i,j}$  represent  $q'$ , or  $q' = f_A$  then  $\mathbf{c}_f$  is true and all  $\mathbf{c}_{i,j}$  are false.  $\mathbf{b}_m, \dots, \mathbf{b}_1$  are just a binary encoding of  $\Sigma_c$ . We reserve a special value of  $\mathbf{b}$  that is  $\mathbf{b}_i$  are all false for  $\perp$ .

We use vector  $\mathbf{a}^i, \mathbf{b}^i, \mathbf{c}^i$  to encode each  $\delta_i$  ( $0 \leq i \leq k$ ) in a run  $\delta_0 \xrightarrow{*} \delta_k$ . Note that superscript is used to represent iteration index of vectors, and subscript is used to represent index in vectors. For example, a propositional variable  $\mathbf{a}_{1,1}^0$  represents whether a pair  $(1, 1)$  is in  $q_0$  ( $\delta_0 = q_0 \xrightarrow{\perp} f_A$ ), and  $\mathbf{a}_{1,1}^2$  represents whether a pair  $(1, 1)$  is in  $q_2$  after reading  $a_2$  and a run of  $\delta$  is  $\delta_0 \xrightarrow{a_1} \delta_1 \xrightarrow{a_2} \delta_2$  and  $q_2 \xrightarrow{a'} q'_2$ .

### 5.1.3 Encoding of transitions of $\mathcal{P}$ -automaton transition

In previous subsection, we presented how to encode each transition of  $\mathcal{P}$ -automaton. In this subsection, we will present how to encode a run of  $\delta$  on  $w$   $\delta_0 \xrightarrow{*} \delta_k$ .

First, we define some notations:

**Definition 13.** For vector of propositional variables we define notations:

- $\neg$  of vector denotes a conjunction of negative of all indices.
- $\iff$  of vector with the same length denotes a conjunction of  $\iff$  of all indices.

For example,  $\neg \mathbf{a}$  denotes  $\neg \mathbf{a}_{1,1} \wedge \dots \wedge \neg \mathbf{a}_{n,n}$  and  $\mathbf{a} \iff \mathbf{a}'$  denotes  $(\mathbf{a}_{1,1} \iff \mathbf{a}'_{1,1}) \wedge \dots \wedge (\mathbf{a}_{n,n} \iff \mathbf{a}'_{n,n})$ .

Then, we define a propositional formula:

$$[\mathcal{P}(M^d)]^k := \text{init}(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0) \wedge \bigwedge_{l=0}^{k-1} \mathcal{T}((\mathbf{a}^l, \mathbf{b}^l, \mathbf{c}^l), (\mathbf{a}^{l+1}, \mathbf{b}^{l+1}, \mathbf{c}^{l+1}))$$

to represent  $\delta_0 \xrightarrow{*} \delta_k$  where  $\text{init}(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0)$  is an encoding of an initial  $\mathcal{P}$ -automaton transition, and each  $\mathcal{T}((\mathbf{a}^l, \mathbf{b}^l, \mathbf{c}^l), (\mathbf{a}^{l+1}, \mathbf{b}^{l+1}, \mathbf{c}^{l+1}))$  represents a transition  $\delta_l \xrightarrow{a^{l+1}} \delta_{l+1}$ . Note that we introduced a variable  $l$  to run an iteration index of vectors, since  $i$  and  $j$  will be used to run an index in vectors later.

The initial  $\mathcal{P}$ -automaton transition  $\delta_0$  is  $Id_{Q_0} \xrightarrow{\perp} f_A$  that represents an initial configuration

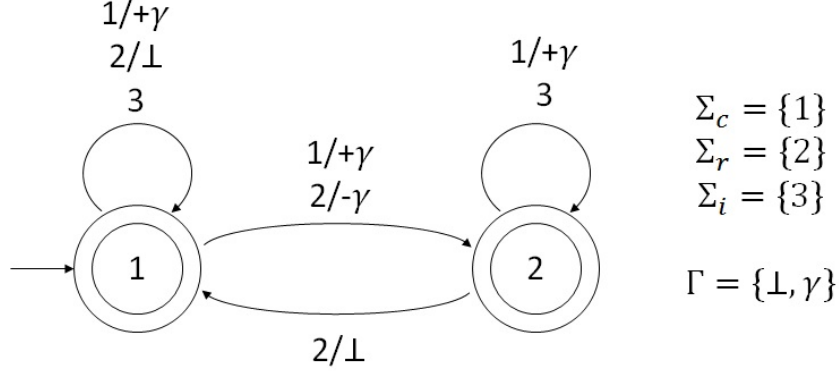


Figure 5.1: An example VPA for checking universality

$(Id_{Q_0}, \perp)$  of  $M^d$ . The formula  $init$  is defined as follow:

$$init(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0) := \bigwedge_{i=1}^n \bigwedge_{j=1}^n f_{init}(i, j) \wedge (\neg \mathbf{b}^0) \wedge (\mathbf{c}^0 \iff (true, false, \dots, false))$$

$$f_{init}(i, j) := \begin{cases} \mathbf{a}_{i,j}^0 & \text{if } i = j \text{ and } i \in Q_0 \\ \neg \mathbf{a}_{i,j}^0 & \text{otherwise} \end{cases}$$

The function  $f_{init}$  represents an initial state  $Id_{Q_0}$  by vector  $\mathbf{a}^0$ . Follow the definition,  $\mathbf{a}_{i,j}^0$  is true iff  $(i, j) \in Id_{Q_0}$ . Variables in vector  $\mathbf{b}^0$  are all false to represent  $\perp$ . Only  $\mathbf{c}_f$  is true in vector  $\mathbf{c}^0$  to represent  $f_A$ .

**Example 10.** From VPA  $M$  in figure 5.1,  $|Q| = 2$  so vector  $\mathbf{a}$  has four indices that are  $(\mathbf{a}_{1,1}, \mathbf{a}_{1,2}, \mathbf{a}_{2,1}, \mathbf{a}_{2,2})$ ,  $\mathbf{b} = (\mathbf{b}_2, \mathbf{b}_1)$  and  $\mathbf{c} = (\mathbf{c}_f, \mathbf{c}_{1,1}, \mathbf{c}_{1,2}, \mathbf{c}_{2,1}, \mathbf{c}_{2,2})$ . Note that since  $|\Sigma_c| = 1$ , actually we can define vector  $\mathbf{b}$  with only one index, but for better intention and understanding we define  $\mathbf{b}$  with two indices to be able to represent all alphabets 1, 2 and 3. The initial state of  $M^d$  is  $\{(1, 1)\}$ , so the initial transition of  $\mathcal{P}$ -automaton is  $\{(1, 1)\} \xrightarrow{\perp} f_A$ . The formula  $init(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0)$  is  $\mathbf{a}_{1,1}^0 \wedge \neg \mathbf{a}_{1,2}^0 \wedge \neg \mathbf{a}_{2,1}^0 \wedge \neg \mathbf{a}_{2,2}^0 \wedge \neg \mathbf{b}_2^0 \wedge \neg \mathbf{b}_1^0 \wedge \mathbf{c}_f^0 \wedge \neg \mathbf{c}_{1,1}^0 \wedge \neg \mathbf{c}_{1,2}^0 \wedge \neg \mathbf{c}_{2,1}^0 \wedge \neg \mathbf{c}_{2,2}^0$ . Therefore,  $\mathbf{a}^0, \mathbf{b}^0$  and  $\mathbf{c}^0$  are  $(true, false, false, false), (false, false)$  and  $(true, false, false, false, false)$ , respectively.

The formula  $\mathcal{T}((\mathbf{a}^l, \mathbf{b}^l, \mathbf{c}^l), (\mathbf{a}^{l+1}, \mathbf{b}^{l+1}, \mathbf{c}^{l+1}))$  represents a transition  $\delta_l \xrightarrow{a^{l+1}} \delta_{l+1}$ .  $\mathcal{T}$  can be divided into four cases based on type of  $\rightarrow$ .

$$\mathcal{T}((\mathbf{a}^l, \mathbf{b}^l, \mathbf{c}^l), (\mathbf{a}^{l+1}, \mathbf{b}^{l+1}, \mathbf{c}^{l+1})) := \mathcal{T}_c \vee \mathcal{T}_i \vee \mathcal{T}_{r_0} \vee \mathcal{T}_r$$

Note that the arguments of  $\mathcal{T}_c, \mathcal{T}_i, \mathcal{T}_{r_0}$  and  $\mathcal{T}_r$  are the same as  $\mathcal{T}$ .

Let  $+$  denotes a concatenation of two vectors and for  $a \in \Sigma$ , let  $be(a)$  denotes a binary encoding of  $a$ . We define formulae for transitions of  $\mathcal{P}$ -automaton transition ( $\rightarrow$ ) as follows:

**Call transition.**

$$\mathcal{T}_c := \bigvee_{a \in \Sigma_c} \left[ \bigwedge_{i=1}^n \bigwedge_{j=1}^n f_c(i, j) \wedge (\mathbf{b}^{l+1} \iff be(a)) \wedge (\mathbf{c}^{l+1} \iff (false) + \mathbf{a}^l) \right]$$

$$f_c(i, j) := \begin{cases} \mathbf{a}_{i,j}^{l+1} \iff \bigvee_{j'} \bigvee_{i'=1}^n \mathbf{a}_{i',j'}^l & \text{if } i = j \text{ and } \exists j' \in Q : j' \xrightarrow{a/\gamma} i \in \Delta_c \\ \neg \mathbf{a}_{i,j}^{l+1} & \text{otherwise} \end{cases}$$

The definition of  $\mathcal{T}_c$  is an encoding of the case  $[c]$  of  $\rightarrow$  (Definition 9 Section 4.4). The definition of function  $f_c$  is to compute state  $Id_{R'}$  from the determinization process. Note that  $\mathcal{T}_c$  has a disjunction of call alphabet because case  $[c]$  of  $\rightarrow$  is for a call transition but not for a specific call alphabet. Each  $a_{l+1} \in \Sigma_c$  can cause  $[c]$  case of  $\delta_l \xrightarrow{a_{l+1}} \delta_{l+1}$ , and the next  $\mathcal{P}$ -automaton transition  $\delta_{l+1}$  depends on  $a_{l+1}$ .

**Internal transition.**

$$\mathcal{T}_i := \bigvee_{a \in \Sigma_i} \left[ \bigwedge_{i=1}^n \bigwedge_{j=1}^n f_i(i, j) \wedge (\mathbf{b}^{l+1} \iff \mathbf{b}^l) \wedge (\mathbf{c}^{l+1} \iff \mathbf{c}^l) \right]$$

$$f_i(i, j) := \begin{cases} \mathbf{a}_{i,j}^{l+1} \iff \bigvee_{j'} \mathbf{a}_{i,j'}^l & \text{if } \exists j' \in Q : j' \xrightarrow{a} j \in \Delta_i \\ \neg \mathbf{a}_{i,j}^{l+1} & \text{otherwise} \end{cases}$$

The definition of  $\mathcal{T}_i$  is an encoding of the case  $[i]$  of  $\rightarrow$  (Definition 9 Section 4.4). The definition of function  $f_i$  is to compute state  $S'$  from the determinization process. Follow the definition of case  $[i]$  of  $\rightarrow$ , the only vector that changes is vector  $\mathbf{a}$  (of source state in  $\delta$ ). Vectors  $\mathbf{b}^{l+1}$  and  $\mathbf{c}^{l+1}$  are the same as  $\mathbf{b}^l$  and  $\mathbf{c}^l$ , respectively.

**Return transition with empty stack.**

$$\mathcal{T}_{r_0} := (\neg \mathbf{b}^l) \wedge \bigvee_{a \in \Sigma_r} \left[ \bigwedge_{i=1}^n \bigwedge_{j=1}^n f_{r_0}(i, j) \wedge (\neg \mathbf{b}^{l+1}) \wedge (\mathbf{c}^{l+1} \iff (true, false, \dots, false)) \right]$$

$$f_{r_0}(i, j) := \begin{cases} \mathbf{a}_{i,j}^{l+1} \iff \bigvee_{j'} \mathbf{a}_{i,j'}^l & \text{if } \exists j' \in Q : j' \xrightarrow{a/\perp} j \in \Delta_r \\ \neg \mathbf{a}_{i,j}^{l+1} & \text{otherwise} \end{cases}$$

The definition of  $\mathcal{T}_{r_0}$  is an encoding of the case  $[r_0]$  of  $\rightarrow$  (Definition 9 Section 4.4). First of all,  $\neg \mathbf{b}^l$  (denotes  $\perp$ ) is a necessary condition to check whether the current stack is empty. Case  $[r_0]$  is really similar to case  $[i]$ . Vector  $\mathbf{a}^{l+1}$  is computed in the same way as case  $[i]$ , but  $\mathbf{b}^{l+1}$  is all false to denote  $\perp$  and only  $\mathbf{c}_f^{l+1}$  in  $\mathbf{c}^{l+1}$  is true to denote  $f_A$ .

**Return transition.**

Let vector  $\mathbf{c}'$  be a vector  $\mathbf{c}$  without a variable  $\mathbf{c}_f$ .

$$\mathcal{T}_r := \bigvee_{a \in \Sigma_r} \bigvee_{a' \in \Sigma_c} [(\mathbf{b}^l \iff be(a')) \wedge \bigwedge_{i=1}^n \bigwedge_{j=1}^n f_r(i, j)] \wedge \bigvee_{l'=0}^l [(\mathbf{c}^{l'} \iff \mathbf{a}^{l'}) \wedge (\mathbf{b}^{l+1} \iff \mathbf{b}^{l'}) \wedge (\mathbf{c}^{l+1} \iff \mathbf{c}^{l'})]$$

$$f_r(i, j) := \begin{cases} \mathbf{a}_{i,j}^{l+1} \iff \bigvee_{i'} \bigvee_{j''} \bigvee_{j'} [\mathbf{c}_{i,j''}^l \wedge \mathbf{a}_{i',j'}^l] & \text{if } \exists i', j', j'' \in Q, \exists \gamma \in \Gamma \setminus \perp : \\ & j'' \xrightarrow{a'/+\gamma} i' \in \Delta_c \text{ and } j' \xrightarrow{a'/-\gamma} j \in \Delta_r \\ \neg \mathbf{a}_{i,j}^{l+1} & \text{otherwise} \end{cases}$$

The definition of  $\mathcal{T}_r$  is an encoding of the case  $[r]$  of  $\rightarrow$  (Definition 9 Section 4.4). Return transition is the most complicated. First, formula  $(\mathbf{b}^l \iff be(a'))$  is used to check top of stack.  $\mathcal{T}_r$  has two disjunctions, one of return alphabet and one of call alphabet which is the top of stack. This is because case  $[r]$  does not depend on only alphabet but alphabet and top of stack. The definition of function  $f_r$  is to compute state  $S'$  from the determinization process. Both current state and top of stack must be considered to compute next state. This result in  $\mathbf{a}^{l+1}$  has to consider both  $\mathbf{a}^l$  and  $\mathbf{c}^l$ . The disjunction over  $l'$  on the second line is used to find the condition “ $S \xrightarrow{a'} S'' \xrightarrow{a''} S''' \in \nabla$ ” in the previous  $\mathcal{P}$ -automaton transition  $\delta_0$  to current  $\mathcal{P}$ -automaton transition  $\delta_l$ .

**Example 11.** Recall the same example, from Figure 5.1, the encoding of transition are:

$$\mathcal{T}_c := (\mathbf{a}_{1,1}^{l+1} \iff \mathbf{a}_{1,1}^l \vee \mathbf{a}_{2,1}^l) \wedge \neg \mathbf{a}_{1,2}^{l+1} \wedge \neg \mathbf{a}_{2,1}^{l+1} \wedge (\mathbf{a}_{2,2}^{l+1} \iff \mathbf{a}_{1,1}^l \vee \mathbf{a}_{1,2}^l \vee \mathbf{a}_{2,1}^l \vee \mathbf{a}_{2,2}^l) \wedge \neg \mathbf{b}_2^{l+1} \wedge \mathbf{b}_1^{l+1} \wedge \neg \mathbf{c}_f^{l+1} \wedge$$

$$(\mathbf{c}_{1,1}^{l+1} \iff \mathbf{a}_{1,1}^l) \wedge (\mathbf{c}_{1,2}^{l+1} \iff \mathbf{a}_{1,2}^l) \wedge (\mathbf{c}_{2,1}^{l+1} \iff \mathbf{a}_{2,1}^l) \wedge (\mathbf{c}_{2,2}^{l+1} \iff \mathbf{a}_{2,2}^l)$$

$$\mathcal{T}_i := (\mathbf{a}_{1,1}^{l+1} \iff \mathbf{a}_{1,1}^l) \wedge (\mathbf{a}_{1,2}^{l+1} \iff \mathbf{a}_{1,2}^l) \wedge (\mathbf{a}_{2,1}^{l+1} \iff \mathbf{a}_{2,1}^l) \wedge (\mathbf{a}_{2,2}^{l+1} \iff \mathbf{a}_{2,2}^l) \wedge$$

$$(\mathbf{b}_2^{l+1} \iff \mathbf{b}_2^l) \wedge (\mathbf{b}_1^{l+1} \iff \mathbf{b}_1^l) \wedge (\mathbf{c}_f^{l+1} \iff \mathbf{c}_f^l) \wedge$$

$$(\mathbf{c}_{1,1}^{l+1} \iff \mathbf{c}_{1,1}^l) \wedge (\mathbf{c}_{1,2}^{l+1} \iff \mathbf{c}_{1,2}^l) \wedge (\mathbf{c}_{2,1}^{l+1} \iff \mathbf{c}_{2,1}^l) \wedge (\mathbf{c}_{2,2}^{l+1} \iff \mathbf{c}_{2,2}^l)$$

$$\mathcal{T}_{r_0} := \neg \mathbf{b}_2^l \wedge \neg \mathbf{b}_1^l \wedge$$

$$(\mathbf{a}_{1,1}^{l+1} \iff \mathbf{a}_{1,1}^l \vee \mathbf{a}_{1,2}^l) \wedge \neg \mathbf{a}_{1,2}^{l+1} \wedge (\mathbf{a}_{2,1}^{l+1} \iff \mathbf{a}_{2,1}^l \vee \mathbf{a}_{2,2}^l) \wedge \neg \mathbf{a}_{2,2}^{l+1} \wedge \neg \mathbf{b}_2^{l+1} \wedge \neg \mathbf{b}_1^{l+1} \wedge \mathbf{c}_f^{l+1} \wedge \neg \mathbf{c}_{1,1}^{l+1} \wedge \neg \mathbf{c}_{1,2}^{l+1} \wedge \neg \mathbf{c}_{2,1}^{l+1} \wedge \neg \mathbf{c}_{2,2}^{l+1}$$

$$\mathcal{T}_r := (\mathbf{b}_2^l \iff false) \wedge (\mathbf{b}_1^l \iff true) \wedge$$

$$\neg \mathbf{a}_{1,1}^{l+1} \wedge (\mathbf{a}_{1,2}^{l+1} \iff (\mathbf{a}_{1,1}^l \wedge \mathbf{c}_{1,1}^l) \vee (\mathbf{a}_{2,1}^l \wedge \mathbf{c}_{1,1}^l) \vee (\mathbf{a}_{2,1}^l \wedge \mathbf{c}_{1,2}^l)) \wedge$$

$$\neg \mathbf{a}_{2,1}^{l+1} \wedge (\mathbf{a}_{2,2}^{l+1} \iff (\mathbf{a}_{1,1}^l \wedge \mathbf{c}_{2,1}^l) \vee (\mathbf{a}_{2,1}^l \wedge \mathbf{c}_{2,1}^l) \vee (\mathbf{a}_{2,1}^l \wedge \mathbf{c}_{2,2}^l)) \wedge$$

$$[\{(\mathbf{c}_{1,1}^l \iff \mathbf{a}_{1,1}^0) \wedge (\mathbf{c}_{1,2}^l \iff \mathbf{a}_{1,2}^0) \wedge (\mathbf{c}_{2,1}^l \iff \mathbf{a}_{2,1}^0) \wedge (\mathbf{c}_{2,2}^l \iff \mathbf{a}_{2,2}^0) \wedge$$

$$(\mathbf{b}_2^{l+1} \iff \mathbf{b}_2^0) \wedge (\mathbf{b}_1^{l+1} \iff \mathbf{b}_1^0) \wedge (\mathbf{c}_f^{l+1} \iff \mathbf{c}_f^0) \wedge$$

$$(\mathbf{c}_{1,1}^{l+1} \iff \mathbf{c}_{1,1}^0) \wedge (\mathbf{c}_{1,2}^{l+1} \iff \mathbf{c}_{1,2}^0) \wedge (\mathbf{c}_{2,1}^{l+1} \iff \mathbf{c}_{2,1}^0) \wedge (\mathbf{c}_{2,2}^{l+1} \iff \mathbf{c}_{2,2}^0)\} \vee$$

$$\dots (\text{until } l' = l) ]$$

where the  $[ ]$  bracket in  $\mathcal{T}_r$  is the disjunction  $l'$  part in the  $\mathcal{T}_r$  defined above. Actually, it must repeat the formula in  $\{ \}$   $l + 1$  times from 0 to  $l$ .

Recall the vectors  $\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0$  from Example 10, when compute for  $\mathbf{a}^1, \mathbf{b}^1, \mathbf{c}^1$ :

- If  $\mathcal{T}_c$  is chosen, then vectors  $\mathbf{a}^1, \mathbf{b}^1$  and  $\mathbf{c}^1$  will become (true, false, false, true), (false, true) and (false, true, false, false, false) respectively that represent  $\mathcal{P}$ -automaton transition  $\delta_1 = \{(1, 1), (2, 2)\} \xrightarrow{1} \{(0, 0)\}$ .
- If  $\mathcal{T}_i$  is chosen, then vectors  $\mathbf{a}^1, \mathbf{b}^1$  and  $\mathbf{c}^1$  will become (true, false, false, false), (false, false) and (true, false, false, false, false) respectively that represent  $\mathcal{P}$ -automaton transition  $\delta_1 = \{(0, 0)\} \xrightarrow{\perp} f_A$ .
- If  $\mathcal{T}_{r_0}$  is chosen, then vectors  $\mathbf{a}^1, \mathbf{b}^1$  and  $\mathbf{c}^1$  will become the same as internal case.
- $\mathcal{T}_r$  cannot be chosen because  $\mathbf{b}_2^0$  and  $\mathbf{b}_1^0$  are false. If  $\mathcal{T}_r$  is chosen, the formula will become unsatisfiable immediately.

#### 5.1.4 Encoding of non-universality checking

In this subsection, we define a propositional formula that is satisfiable if  $\delta_k$  from a run  $\delta_0 \xrightarrow{w^*} \delta_k$  is rejecting. That means the formula is satisfiable if  $M^d$  is not universal.

$$[rej(M^d)]^k := \bigwedge_{i=1}^n \bigwedge_{j \in F} \neg \mathbf{a}_{i,j}^k$$

Because set of final states of  $M^d$  is  $F' = \{S \in Q' \mid \Pi_2(S) \cap F \neq \emptyset\}$ . Intuitively, state  $q$  of  $M^d$  is not a final state if every second component of pair in  $q$  is not in  $F$  regardless of the first component. Extend to  $\mathcal{P}$ -automaton transition  $\delta, \delta = p \xrightarrow{a} p'$  is rejecting if every second component of pair in  $p$  is not in  $F$  regardless of the first component. Thus, to check whether  $\delta_k$  is rejecting, we need to check that every variable  $\mathbf{a}_{i,j}^k$  is false, for  $j \in F$  regardless of  $i$ .

Finally, we sum all formulae above:

$$[notUniversal(M^d)]^k := [\mathcal{P}(M^d)]^k \wedge [rej(M^d)]^k$$

This formula  $[notUniversal(M^d)]^k$  is used to check non-universality of VPAs. It is satisfiable if the given VPA is not universal. In particular, there exists  $w \in \Sigma^k$  that a run of  $\delta$  on  $w, \delta_0 \xrightarrow{w^*} \delta_k$ , ends up with a rejecting  $\mathcal{P}$ -automaton transition.

Note that because we start a bound from  $k = 0$ , if at some  $k, [notUniversal(M^d)]^k$  is satisfiable, that means there exists a counter-example of input length  $|w| = k$  and of course that counter-example is the shortest.

**Example 12.** Recall the same example again, from figure 5.1, formula  $[rej(M^d)]^k := \neg \mathbf{a}_{1,1}^k \wedge \neg \mathbf{a}_{1,2}^k \wedge \neg \mathbf{a}_{2,1}^k \wedge \neg \mathbf{a}_{2,2}^k$ . This means the only state that is not a final state of  $M^d$  is  $q = \emptyset$  ( $\mathbf{a}_{i,j}$  are all false).

For this VPA, the shortest counter-example is ‘1212’. The formula  $[notUniversal(M^d)]^k$  is satisfiable if  $k = 4$  with choice of transition  $\mathcal{T}_c, \mathcal{T}_r, \mathcal{T}_c, \mathcal{T}_r$ , respectively. Vectors  $\mathbf{a}^4, \mathbf{b}^4$  and  $\mathbf{c}^4$  become (false, false, false, false), (false, false), (true, false, false, false, false) respectively. This means  $\delta_4 = \emptyset \xrightarrow{\perp} f_A$  which is a rejecting  $\mathcal{P}$ -automaton transition.

## 5.2 Antichain for non-universality checking

In this section, we will explain theoretical backgrounds why we can apply antichain to the transition formula  $\mathcal{T}$  when checking for non-universality, and this results in replacing some  $\iff$  in  $\mathcal{T}$  with  $\implies$ .

First, we define an ordering over  $\mathcal{P}$ -automaton transition.

**Definition 14** (Partial ordering over  $\mathcal{P}$ -automaton transition of  $M^d$ ). Let  $\delta_1 = p_1 \xrightarrow{a} p'_1$  and  $\delta_2 = p_2 \xrightarrow{a} p'_2$  be transitions of  $\mathcal{P}$ -automaton of determinized VPA  $M^d$ , we say  $\delta_1 \leq \delta_2$  if  $p_1 \leq p_2$  and  $p'_1 \leq p'_2$  wrt ordering over states of  $M^d$  from definition 6 (Section 3.2.2). Note that if  $p'_1 = p'_2 = f_A$ , we also say  $p'_1 \leq p'_2$ .

From definition 14, we start with establishing theoretical background.

**Lemma 10.** Let  $\delta_1 = p_1 \xrightarrow{a} p'_1$ ,  $\delta'_1 = p'_1 \xrightarrow{a'} p''_1$ ,  $\delta_2 = p_2 \xrightarrow{a} p'_2$ , and  $\delta'_2 = p'_2 \xrightarrow{a'} p''_2$  be transitions of  $\mathcal{P}$ -automaton of  $M^d$ . Let  $\delta_1 \xrightarrow{a''} \delta''_1$  and  $\delta_2 \xrightarrow{a''} \delta''_2$  be transitions of  $\mathcal{P}$ -automaton transition follow definition 9 (Section 4.4). If  $\delta_1 \leq \delta_2$  and  $\delta'_1 \leq \delta'_2$ , then  $\delta''_1 \leq \delta''_2$ .

*Proof.* We have four cases.

- [c] transition: by definition, we have  $p_1 = S_1, p_2 = S_2, \delta''_1 = Id_{R'_1} \xrightarrow{a''} S_1$  and  $\delta''_2 = Id_{R'_2} \xrightarrow{a''} S_2$  that  $S_1 \leq S_2$ . From lemma 2 (Section 3.2.2), we have  $Id_{R'_1} \leq Id_{R'_2}$ . Thus,  $\delta''_1 \leq \delta''_2$ .
- [i] transition: by definition, we have  $p_1 = S_1, p_2 = S_2, \delta''_1 = S'_1 \xrightarrow{a} p'_1$  and  $\delta''_2 = S'_2 \xrightarrow{a} p'_2$  that  $S_1 \leq S_2$  and  $p'_1 \leq p'_2$ . From lemma 3 (Section 3.2.2), we have  $S'_1 \leq S'_2$ . Thus,  $\delta''_1 \leq \delta''_2$ .
- [ $r_0$ ] transition: by definition, we have  $p_1 = S_1, p_2 = S_2, \delta''_1 = S'_1 \xrightarrow{\perp} f_A$  and  $\delta''_2 = S'_2 \xrightarrow{\perp} f_A$  that  $S_1 \leq S_2$ . From lemma 4 (Section 3.2.2), we have  $S'_1 \leq S'_2$ . Thus,  $\delta''_1 \leq \delta''_2$ .



- $[r]$  transition: by definition, we have  $p_1 = S_1, p'_1 = S''_1, p_2 = S_2, p'_2 = S''_2, \delta''_1 = S'_1 \xrightarrow{x'} p''_1$  and  $\delta''_2 = S'_2 \xrightarrow{x'} p''_2$  that  $S_1 \leq S_2, S''_1 \leq S''_2$  and  $p''_1 \leq p''_2$ . From lemma 5 (Section 3.2.2), we have  $S'_1 \leq S'_2$ . Thus,  $\delta''_1 \leq \delta''_2$ .  $\square$

Now, we extend lemma to a run of  $\delta$ .

**Lemma 11.** *Let  $\delta_0 = q_0 \xrightarrow{\perp} f_{\mathcal{A}}$  be an unique initial  $\mathcal{P}$ -automaton transition. and let  $\delta_0 \xrightarrow{w} \delta_k$  be a run of  $\delta$  on  $w$ . Suppose  $\delta_0 \xrightarrow{w} \delta'_k$  be a run of  $\delta$  on  $w$  that  $\delta_i \leq \delta'_i$  for every  $1 \leq i \leq k$ . For any word  $w' \in \Sigma^*$ , if a run of  $\delta$  on  $ww'$  are  $\delta_0 \xrightarrow{w} \delta_k \xrightarrow{w'} \delta_m$  and  $\delta_0 \xrightarrow{w} \delta'_k \xrightarrow{w'} \delta'_m$ , then  $\delta_i \leq \delta'_i$  for every  $1 \leq i \leq m$ .*

*Proof.* We prove this lemma by induction on length  $|w'|$ . For  $|w'| = 1$ , the lemma holds immediately from lemma 10. For induction case, assume the lemma holds for  $w$ , we can prove for  $wa$  by using induction hypothesis and lemma 10 wrt type of  $a$ .  $\square$

**Lemma 12.** *Let  $\delta_1$  and  $\delta_2$  are  $\mathcal{P}$ -automaton transition that  $\delta_1 \leq \delta_2$ . If  $\delta_2$  is rejecting, then  $\delta_1$  is also rejecting.*

*Proof.* Let  $\delta_1 = p_1 \xrightarrow{a} p'_1$  and  $\delta_2 = p_2 \xrightarrow{a} p'_2$ . Recall that  $\delta_2$  is rejecting if  $\Pi_2(p_2) \cap F = \emptyset$ . Note that  $p_1 \leq p_2$ , so  $\Pi_2(p_1) \cap F = \emptyset$ .  $\square$

From lemma 11 and lemma 12, we apply antichain to encoding of transitions of  $\mathcal{P}$ -automaton transition, i.e., apply to propositional formula  $\mathcal{T}$ . We change the logical operator  $\iff$  (equivalence) in  $f_c, f_i, f_{r_0}$  and  $f_r$  to be just  $\impliedby$  (implication).

$$\begin{aligned}
\mathcal{T}_c &:= \bigvee_{a \in \Sigma_c} \left[ \bigwedge_{i=1}^n \bigwedge_{j=1}^n f_c(i, j) \wedge (\mathbf{b}^{l+1} \iff be(a)) \wedge (\mathbf{c}^{l+1} \iff (false) + \mathbf{a}^l) \right] \\
f_c(i, j) &:= \begin{cases} \mathbf{a}_{i,j}^{l+1} \iff \bigvee_{j'} \bigvee_{i'=1}^n \mathbf{a}_{i',j'}^l & \text{if } i = j \text{ and } \exists j' \in Q : j' \xrightarrow{a/\gamma} i \in \Delta_c \\ \neg \mathbf{a}_{i,j}^{l+1} & \text{otherwise} \end{cases} \\
\mathcal{T}_i &:= \bigvee_{a \in \Sigma_i} \left[ \bigwedge_{i=1}^n \bigwedge_{j=1}^n f_i(i, j) \wedge (\mathbf{b}^{l+1} \iff \mathbf{b}^l) \wedge (\mathbf{c}^{l+1} \iff \mathbf{c}^l) \right] \\
f_i(i, j) &:= \begin{cases} \mathbf{a}_{i,j}^{l+1} \iff \bigvee_{j'} \mathbf{a}_{i,j'}^l & \text{if } \exists j' \in Q : j' \xrightarrow{a} j \in \Delta_i \\ \neg \mathbf{a}_{i,j}^{l+1} & \text{otherwise} \end{cases} \\
\mathcal{T}_{r_0} &:= (\neg \mathbf{b}^l) \wedge \bigvee_{a \in \Sigma_r} \left[ \bigwedge_{i=1}^n \bigwedge_{j=1}^n f_{r_0}(i, j) \wedge (\neg \mathbf{b}^{l+1}) \wedge (\mathbf{c}^{l+1} \iff (true, false, \dots, false)) \right] \\
f_{r_0}(i, j) &:= \begin{cases} \mathbf{a}_{i,j}^{l+1} \iff \bigvee_{j'} \mathbf{a}_{i,j'}^l & \text{if } \exists j' \in Q : j' \xrightarrow{a/\perp} j \in \Delta_r \\ \neg \mathbf{a}_{i,j}^{l+1} & \text{otherwise} \end{cases} \\
\mathcal{T}_r &:= \bigvee_{a \in \Sigma_r} \bigvee_{a' \in \Sigma_c} \left[ (\mathbf{b}^l \iff be(a')) \wedge \bigwedge_{i=1}^n \bigwedge_{j=1}^n f_r(i, j) \wedge \right. \\
&\quad \left. \bigvee_{l'=0}^l [(\mathbf{c}^{l'} \iff \mathbf{a}^{l'}) \wedge (\mathbf{b}^{l+1} \iff \mathbf{b}^{l'}) \wedge (\mathbf{c}^{l+1} \iff \mathbf{c}^{l'})] \right] \\
f_r(i, j) &:= \begin{cases} \mathbf{a}_{i,j}^{l+1} \iff \bigvee_{i'} \bigvee_{j''} \bigvee_{j'} [\mathbf{c}_{i,j''}^l \wedge \mathbf{a}_{i',j'}^l] & \text{if } \exists i', j', j'' \in Q, \exists \gamma \in \Gamma \setminus \perp : \\ & j'' \xrightarrow{a'/\gamma} i' \in \Delta_c \text{ and } j' \xrightarrow{a'/-\gamma} j \in \Delta_r \\ \neg \mathbf{a}_{i,j}^{l+1} & \text{otherwise} \end{cases}
\end{aligned}$$

*Remark.* This change from  $\iff$  to  $\implies$  of a bit representation of set means that when checking the satisfiability, for each  $\delta p \xrightarrow{a} p''$ , SAT-solver can construct  $p' \xrightarrow{a} p''$  that  $p \leq p'$  instead. It is crucial to note that this is safe because if the formula is satisfiable that means  $p' \xrightarrow{a} p''$  can lead to the counter-example, from lemma 11 and 12 so does  $p \xrightarrow{a} p''$ . Furthermore, if  $p' \xrightarrow{a} p''$  is ‘too large’ to lead to the counter-example, SAT-solver can construct it smaller and the smallest  $p'$  that SAT-solver can construct is actually  $p' = p$ .

Although this change seems very little, it results in a big decrease of number of variables and clauses when converse all formulae to CNF to be an input to SAT-solver (See Figure 6.1 and 6.2 in Chapter 6 for experimental results).

### 5.3 BMCi and BMCp for non-universality checking

Recall that BMCi unrolls  $M^d$  based on bounded transitions of  $\mathcal{P}$ -automaton transition ( $\rightarrow$ ) but BMCp unrolled  $M^d$  based on bounded  $\mathcal{P}$ -automaton saturation process ( $\Rightarrow$ ).

#### BMCi

Since BMCi uses  $\rightarrow$  to unroll  $M^d$ , we can use the formula  $[\mathcal{P}(M^d)]^k$  to encode transitions. Furthermore, checking non-universality process of BMCi can use the formula  $[notUniversal(M^d)]^k$  directly.

We define an algorithm BMCi based on encoding above.

---

#### Algorithm 5: BMCi

---

**Data:** A non-deterministic VPA  $M = (Q, Q_0, \Gamma, F, \Delta)$

**Result:** Universality of  $M$

**begin**

$k \leftarrow 0$

Construct  $[notUniversal(M^d)]^0 := init(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0) \wedge [rej(M^d)]^0$

Check for satisfiability of  $[notUniversal(M^d)]^0$

**while**  $[notUniversal(M^d)]^k$  is unsatisfiable **do**

$k \leftarrow k + 1$

Construct  $[notUniversal(M^d)]^k :=$

$init(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0) \wedge \bigwedge_{i=0}^{k-1} \mathcal{T}((\mathbf{a}^i, \mathbf{b}^i, \mathbf{c}^i), (\mathbf{a}^{i+1}, \mathbf{b}^{i+1}, \mathbf{c}^{i+1})) \wedge [rej(M^d)]^k$

Check for satisfiability of  $[notUniversal(M^d)]^k$

**end**

**return** *False*

**end**

---

#### BMCp

BMCp transitions are based on  $\mathcal{P}$ -automaton saturation process  $\mathcal{A}_0 \Rightarrow \dots \Rightarrow \mathcal{A}_k$  (denoted by  $\mathcal{A}_0 \Rightarrow_k^* \mathcal{A}_k$ ), where  $\mathcal{A}_0$  is an unique initial  $\mathcal{P}$ -automaton of  $M^d$  accepting the initial configuration  $(Id_{Q_0}, \perp)$ ,  $\mathcal{A}_0 = (\{Id_{Q_0}\}, \Sigma_c, \{Id_{Q_0}\}, \{f_A\}, \{(Id_{Q_0}, \perp), f_A\})$ .

**Theorem 7.**  $M^d$  is not universal iff there exists  $k \in \mathbb{N}$  that  $\mathcal{A}_0 \Rightarrow_k^* \mathcal{A}_k$  and  $\mathcal{A}_k$  contains a rejecting state  $q \notin F'$

we define a new propositional formula:

$$[\mathcal{P}'(M^d)]^k := \text{init}(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0) \wedge \bigwedge_{l=0}^{k-1} \mathcal{T}'((\mathbb{A}^l, \mathbb{B}^l, \mathbb{C}^l), (\mathbf{a}^{l+1}, \mathbf{b}^{l+1}, \mathbf{c}^{l+1}))$$

to represent  $\mathcal{A}_0 \Rightarrow_k^* \mathcal{A}_k$ , and a formula for non-universality checking for BMCp is:

$$[\text{notUniversal}'(M^d)]^k := [\mathcal{P}'(M^d)]^k \wedge [\text{rej}(M^d)]^k$$

The formula  $\text{init}$  is the same as that in BMCi, since the initial  $\mathcal{P}$ -automaton  $\mathcal{A}_0$  contains an initial  $\mathcal{P}$ -automaton transition only.

The formula  $\mathcal{T}'$  that represents a transition  $\mathcal{A}_l \Rightarrow \mathcal{A}_{l+1}$ , is defined as:

$$\mathcal{T}'((\mathbb{A}^l, \mathbb{B}^l, \mathbb{C}^l), (\mathbf{a}^{l+1}, \mathbf{b}^{l+1}, \mathbf{c}^{l+1})) := \text{union}^l(\mathbb{A}^l, \mathbb{B}^l, \mathbb{C}^l) \wedge \mathcal{T}((\mathbb{A}^l, \mathbb{B}^l, \mathbb{C}^l), (\mathbf{a}^{l+1}, \mathbf{b}^{l+1}, \mathbf{c}^{l+1}))$$

Intuitively, we can look at the transition  $\delta \rightarrow \delta'$  as  $\delta'$  is an immediate successor of  $\delta$ , i.e., if  $\delta \rightarrow \delta'$  and  $\delta$  is in  $\mathcal{A}_i$ , then  $\delta'$  can be in  $\mathcal{A}_{i+1}$  (up to choice of saturation rules  $[c]$ ,  $[i]$ ,  $[r_0]$  or  $[r]$ ). Therefore, we can extend the formula  $\mathcal{T}$  to saturation process  $\Rightarrow$  of  $\mathcal{P}$ -automaton, with one condition that  $\mathbf{a}^{l+1}, \mathbf{b}^{l+1}, \mathbf{c}^{l+1}$  must be computed from  $(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0), (\mathbf{a}^1, \mathbf{b}^1, \mathbf{c}^1), \dots, (\mathbf{a}^l, \mathbf{b}^l, \mathbf{c}^l)$  (not only from  $\mathbf{a}^l, \mathbf{b}^l, \mathbf{c}^l$  as in  $[\mathcal{P}(M^d)]^k$ ).

We define the formula  $\text{union}^l$  as:

$$\text{union}^l(\mathbb{A}^l, \mathbb{B}^l, \mathbb{C}^l) := \bigvee_{i=0}^l [(\mathbb{A}^l \iff \mathbf{a}^i) \wedge (\mathbb{B}^l \iff \mathbf{b}^i) \wedge (\mathbb{C}^l \iff \mathbf{c}^i)]$$

The new vectors  $\mathbb{A}^l, \mathbb{B}^l, \mathbb{C}^l$  and formula  $\text{union}^l$  are used to combine  $(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0)$  to  $(\mathbf{a}^l, \mathbf{b}^l, \mathbf{c}^l)$  in order to compute  $\mathbf{a}^{l+1}, \mathbf{b}^{l+1}, \mathbf{c}^{l+1}$ . Intuitively,  $\mathbb{A}^l, \mathbb{B}^l, \mathbb{C}^l$  can choose to be  $\mathbf{a}^i, \mathbf{b}^i, \mathbf{c}^i$  for  $0 \leq i \leq l$  in order to compute  $\mathbf{a}^{l+1}, \mathbf{b}^{l+1}, \mathbf{c}^{l+1}$ .

**Example 13.** Recall VPA from figure 5.1, suppose  $k = 2$  the formula  $[\mathcal{P}'(M^d)]^k$  is:

$$[\mathcal{P}'(M^d)]^2 := \text{init}^0(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0) \wedge \text{union}(\mathbb{A}^0, \mathbb{B}^0, \mathbb{C}^0) \wedge \mathcal{T}((\mathbb{A}^0, \mathbb{B}^0, \mathbb{C}^0), (\mathbf{a}^1, \mathbf{b}^1, \mathbf{c}^1)) \wedge \text{union}^1(\mathbb{A}^1, \mathbb{B}^1, \mathbb{C}^1) \wedge \mathcal{T}((\mathbb{A}^1, \mathbb{B}^1, \mathbb{C}^1), (\mathbf{a}^2, \mathbf{b}^2, \mathbf{c}^2))$$

where  $\text{init}^0$  and  $\text{init}^1$  are:

$$\begin{aligned} \text{union}^0(\mathbb{A}^0, \mathbb{B}^0, \mathbb{C}^0) &:= (\mathbb{A}^0 \iff \mathbf{a}^0) \wedge (\mathbb{B}^0 \iff \mathbf{b}^0) \wedge (\mathbb{C}^0 \iff \mathbf{c}^0) \\ \text{union}^1(\mathbb{A}^1, \mathbb{B}^1, \mathbb{C}^1) &:= (\mathbb{A}^1 \iff \mathbf{a}^0) \wedge (\mathbb{B}^1 \iff \mathbf{b}^0) \wedge (\mathbb{C}^1 \iff \mathbf{c}^0) \\ &\quad (\mathbb{A}^1 \iff \mathbf{a}^1) \wedge (\mathbb{B}^1 \iff \mathbf{b}^1) \wedge (\mathbb{C}^1 \iff \mathbf{c}^1) \end{aligned}$$

Note that  $\text{union}^0$  is not necessary, because  $\mathcal{A}_0$  has only one transition  $\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0$ .

**Example 14.** We show an example that  $[\mathcal{P}(M^d)]$  and  $[\mathcal{P}'(M^d)]^k$  are different. For a VPA  $M$  in a Figure 5.2, the initial  $\mathcal{P}$ -automaton transition  $\delta_0(= \mathcal{A}_0)$  is  $\{(1, 1)\} \xrightarrow{\perp} f_A$ . A  $\mathcal{P}$ -automaton transition  $\{(1, 3)\} \xrightarrow{1} \{(1, 3)\}$  is in  $[\mathcal{P}(M^d)]^5$  but  $[\mathcal{P}'(M^d)]^4$ . This is because  $\delta_0 \xrightarrow{12112^*} (\{(1, 3)\} \xrightarrow{1} \{(1, 3)\})$  but  $\{(1, 3)\} \xrightarrow{1} \{(1, 3)\}$  appears since  $\mathcal{A}_4$ .  $\{(1, 5)\} \xrightarrow{\perp} f_A$  is in  $[\mathcal{P}(M^d)]^{10}$  (input is ‘1211211222’) but  $[\mathcal{P}'(M^d)]^6$ , and  $\{(1, 5)\} \xrightarrow{1} \{(1, 3)\}$  is in  $[\mathcal{P}(M^d)]^{13}$  (input is ‘1211211211222’) but also  $[\mathcal{P}'(M^d)]^6$ ,

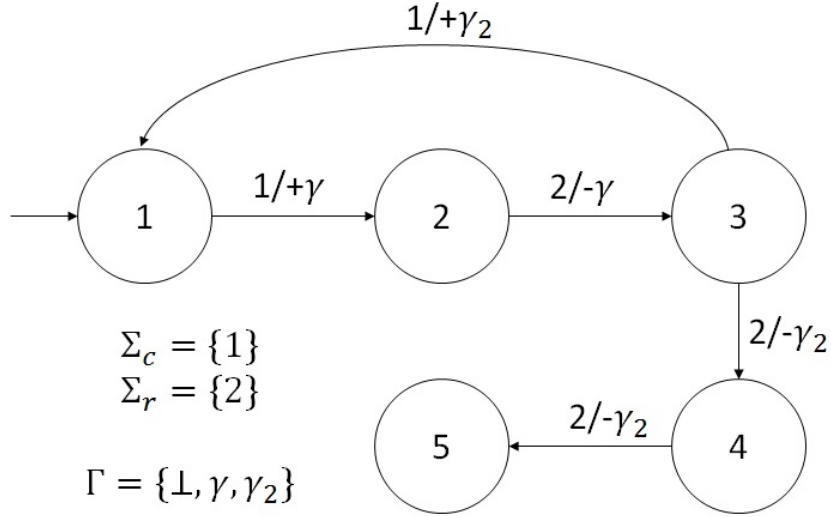


Figure 5.2: An example of VPA that shows differences of  $[\mathcal{P}(M^d)]^k$  and  $[\mathcal{P}'(M^d)]^k$

## 5.4 BMCp for universality checking

*Remark.* It is crucial to note that if there exists a counter-example, BMCi method will eventually find it. The formula  $[notUniversal(M^d)]^k$  will be satisfiable at some bound  $k$ . But if the given VPA is universal, we need to enlarge bound  $k$  until we can confirm that all reachable states of VPA are considered. The smallest  $k$  that has this property is called *reachability diameter* [9, 29]. However, for a VPA with an infinite state space (infinite configuration), there is no such finite reachability diameter [8]. Therefore, BMCi is not complete. It will not terminate for universal cases. Therefore, for universality checking only BMCp will be considered.

For a bound  $k$ , let a  $\mathcal{P}$ -automaton saturation process proceeds  $\mathcal{A}_0 \Rightarrow^* \mathcal{A}_k$ . We can conclude that  $M^d$  is universal if no counter-examples are found in  $\mathcal{A}_k$  and  $\mathcal{A}_k$  is saturated (i.e.,  $\mathcal{A}_k = \mathcal{A}_{k-1}$ ).

We define a propositional formula:

$$[notConverged(M^d)]^k := \bigwedge_{i=1}^k \bigwedge_{j=0}^{i-1} \neg[(\mathbf{a}^i \iff \mathbf{a}^j) \wedge (\mathbf{b}^i \iff \mathbf{b}^j) \wedge (\mathbf{c}^i \iff \mathbf{c}^j)]$$

This formula is satisfiable if  $\mathcal{A}_k$  is not converged, that is  $\mathcal{A}_i \neq \mathcal{A}_j$  for every  $i \leq k$  and  $j \leq i$ .

Finally, the formula for universality checking is:

$$[notSaturated(M^d)]^k := [\mathcal{P}'(M^d)]^k \wedge [notConverged(M^d)]^k$$

This formula is satisfiable if  $\mathcal{A}_k$  is still not saturated, and unsatisfiable if  $\mathcal{A}_k$  is saturated and universal. Note that if  $[notSaturated(M^d)]^k$  is unsatisfiable we can conclude that  $M^d$  is universal immediately without checking  $[notUniversal'(M^d)]^k$ , since  $\mathcal{A}_k = \mathcal{A}_{k-1}$  and  $\mathcal{A}_{k-1}$  does not have any rejecting  $\mathcal{P}$ -automaton transition (if  $\mathcal{A}_{k-1}$  has rejecting  $\mathcal{P}$ -automaton transitions, we could already conclude that  $M^d$  is not universal).

**Example 15.** *We show an example for saturation (without considering universality). The  $M^d$  in Figure 5.2 is saturated in 7 steps with process:*

$$\begin{aligned} \nabla_0 &= \{((1, 1), \perp, f_{\mathcal{A}})\} \\ \nabla_1 &= \nabla_0 \cup \{(\{(2, 2)\}, ((1, 1), 1), \{(1, 1)\})\} \\ \nabla_2 &= \nabla_1 \cup \{(\{(1, 3)\}, \perp, f_{\mathcal{A}})\} \\ \nabla_3 &= \nabla_2 \cup \{(\{(1, 1)\}, ((1, 3), 1), \{(1, 3)\})\} \\ \nabla_4 &= \nabla_3 \cup \{(\{(1, 3)\}, ((1, 3), 1), \{(1, 3)\})\} \\ \nabla_5 &= \nabla_4 \cup \{(\{(1, 4)\}, \perp, f_{\mathcal{A}}), (\{(1, 4)\}, ((1, 3), 1), \{(1, 3)\})\} \\ \nabla_6 &= \nabla_5 \cup \{(\{(1, 5)\}, \perp, f_{\mathcal{A}}), (\{(1, 5)\}, ((1, 3), 1), \{(1, 3)\})\} \\ \nabla_7 &= \nabla_6 \end{aligned}$$

*Thus, when  $i = 7$ , whatever  $\mathbf{a}^7, \mathbf{b}^7, \mathbf{c}^7$  be, it will be equal to one of  $(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0) \dots (\mathbf{a}^6, \mathbf{b}^6, \mathbf{c}^6)$  and  $[notConverged(M^d)]^7$  and  $[notSaturated(M^d)]^7$  are unsatisfiable.*

## 5.5 Antichain for universality checking

From lemma 6 and 7 (Section 3.2.2), we conclude that it is sufficient to keep only minimal configurations in order to check universality of VPA. In antichain algorithm, we also keep only minimal configurations and remove the larger configurations from  $\mathcal{P}$ -automaton (algorithm 3 Section 3.2.2). We apply this to BMCp by changing  $(\mathbf{a}^i \iff \mathbf{a}^j)$  in  $[notConverged(M^d)]^k$  to  $(\mathbf{a}^i \implies \mathbf{a}^j)$ .

$$[notConverged(M^d)]^k := \bigwedge_{i=1}^k \bigwedge_{j=0}^{i-1} \neg[(\mathbf{a}^i \implies \mathbf{a}^j) \wedge (\mathbf{b}^i \iff \mathbf{b}^j) \wedge (\mathbf{c}^i \iff \mathbf{c}^j)]$$

where  $\implies$  of two vectors with the same length denotes conjunction of  $\implies$  of all indices (i.e.,  $(\mathbf{a}_{1,1}^i \implies \mathbf{a}_{1,1}^j) \dots (\mathbf{a}_{n,n}^i \implies \mathbf{a}_{n,n}^j)$ ).

Since we use a bit representation to represent sets, a logical operation  $\implies$  over two vectors denotes a subset relation of two sets. In particular,  $\mathbf{a}^i \implies \mathbf{a}^j$  denotes  $q^i \subseteq q^j$ .

Now, the formula  $[notConverged(M^d)]^k$  is unsatisfiable if all  $\mathcal{P}$ -automaton transitions in  $\mathcal{A}_k$  are not minimal. This is similar to an Algorithm 3 (Section 3.2.2). If all configurations that we have to check are not minimal, we can discard them and  $\mathcal{P}$ -automaton is converged.

Although this change seems very little, it results in a decrease of number of bounds until convergence when checking universal VPAs. (See Figure 6.3 in Chapter 6 for experimental results).

Finally, we define an algorithm BMCp as below:

In conclusion, Differences of BMCi and BMCp are:

- how to bound  $M^d$ , BMCi bounds  $M^d$  based on transitions of  $\mathcal{P}$ -automaton transition but BMCp bounds  $M^d$  based on  $\mathcal{P}$ -automaton saturation process.
- encoding of transitions, BMCi uses the formula  $[\mathcal{P}(M^d)]^k$  but BMCp uses  $[\mathcal{P}'(M^d)]^k$  which has additional vectors and clauses.
- BMCp has to check for satisfiability of formulae two times for each bound  $k$ , that are check for universality and for non-universality, but BMCi checks for non-universality only.
- BMCi is not a complete method but BMCp is. BMCi can check for non-universality, but cannot check for universality of VPAs.

---

**Algorithm 6:** BMCp

---

**Data:** A non-deterministic VPA  $M = (Q, Q_0, \Gamma, F, \Delta)$

**Result:** Universality of  $M$

**begin**

$k \leftarrow 0$

    Construct  $[notUniversal'(M^d)]^0 := init(\mathbf{a}^0, \mathbf{b}^0, \mathbf{c}^0) \wedge [rej(M^d)]^0$

    Check for satisfiability of  $[notUniversal'(M^d)]^0$

**while**  $[notUniversal'(M^d)]^k$  is unsatisfiable **do**

$k \leftarrow k + 1$

        Construct  $[notSaturated(M^d)]^k := [\mathcal{P}'(M^d)]^k \wedge [notConverged(M^d)]^k$

        Check for satisfiability of  $[notSaturated(M^d)]^k$

**if**  $[notSaturated(M^d)]^k$  is unsatisfiable **then**

**return** *True*

**end**

        Construct  $[notUniversal'(M^d)]^k := [\mathcal{P}'(M^d)]^k \wedge [rej(M^d)]^k$

        Check for satisfiability of  $[notUniversal'(M^d)]^k$

**end**

**return** *False*

**end**

---



# Chapter 6

## Experimental results

We have implemented the algorithms BMCi and BMCp. The packages are implemented in Java 1.7.0 on Windows 7 and we use the latest version of MiniSat [25, 26] (i.e., minisat-2.2.0) for checking satisfiability of the formula. The cooperation between Java and MiniSat is that the Java program provides a CNF propositional formula as a text file to be an input for MiniSat according to Algorithm 5 (Section 5.3) and 6 (Section 5.5). Then the Java program calls MiniSat to solve for the satisfiability. After getting the result, either reports whether the given VPA is universal or not universal, or enlarge the bound.

To compare efficiency between BMCi and BMCp, we run our implementations on randomly generated VPA. All tests are performed on a laptop PC, which is equipped with Intel®Core™ i5-2430M 2.4 GHz and 4 GB of memory.

During the experiments, we fix the size of input alphabet to  $|\Sigma_c| = |\Sigma_i| = |\Sigma_r| = 2$  and the size of stack symbol to  $|\Gamma| = 3$ . The density of final states  $f = \frac{|F|}{|Q|}$  (resp. density of initial states  $i = \frac{|Q_0|}{|Q|}$ ) is the ratio of number of final states (resp. initial states) over the number of states of VPA.

### Experiment1: non-universal r-random VPAs

We first set the parameters of the tests as follows:

**Definition 15** (r-random). *An r-random VPA is a VPA that is generated by: first randomly generated transitions labeled with a input symbol (and stack symbol if the input symbol is in return alphabet), then for each transition randomly select two states to be source and destination of the transition. The density of transitions  $r = \frac{|\Delta|}{|Q|}$  is average number of transitions for each state.*

	number of states					
BMCi w/ antichain	5	10	15	20	25	30
number of success	50	50	49	44	39	33
number of timeout	0	0	1	6	11	17
total time(sec)	5	79	54	158	474	442
	number of states					
BMCi w/o antichain	5	10	15	20	25	30
number of success	50	49	49	41	23	17
number of timeout	0	1	1	9	27	33
total time(sec)	6	45	219	354	447	378
	number of states					
BMCp w/ antichain	5	10	15	20	25	30
number of success	50	49	49	43	33	30
number of timeout	0	1	1	7	17	20
total time(sec)	9	50	110	228	501	637
	number of states					
BMCp w/ antichain	5	10	15	20	25	30
number of success	50	48	48	40	21	16
number of timeout	0	2	2	10	29	34
total time(sec)	11	82	327	439	499	512

Table 6.1: Universality checking for not universal VPA generated by r-random (50 automata for each sample)

We ran our tests on r-random VPAs. We set the density of final states and initial state  $f = i = 0.6$ , and the density of transitions  $r = 20$ . This makes the number of transitions  $|\Delta| = 20 \times |Q|$ . We have tried VPA sizes from 5 to 30. We generated 50 VPAs for each sample point and the timeout is set to 60 seconds. The results are shown in table 6.1.

We found that all VPAs are not universal, since r-random VPAs are almost always not universal from random graph theory. The experimental results show that BMCi with antichain algorithm is the best among 4 algorithms. Note that this is not a fair comparison for BMCp, since BMCp has to check for universal before checking non-universal. However, even if BMCp has to check for universal before checking non-universal, BMCp with antichain algorithm is still better than BMCi without antichain algorithm.

Figure 6.1 shows benefits of antichain algorithm in reducing the number of variables and clauses. This is from testing instances with size 5. The maximal benefit appears in testing

instances with size 30, as shown in Figure 6.2. The results show that in maximal case (6th round of size 30 instances) antichain algorithm can reduce number of variables to only 1.5 percent (approx. 0.8 million variables removed), and number of clauses decreases to 58 percent (approx. 1.8 million clauses removed). All of the removed variables and clauses are from conversion-to-CNF procedure [35] (using Tseitin conversion [36]). Since antichain change the transition formula from  $\iff$  to  $\implies$  (Section 5.2), the number of fresh variables and clauses introduced by the conversion procedure decreases a lot.

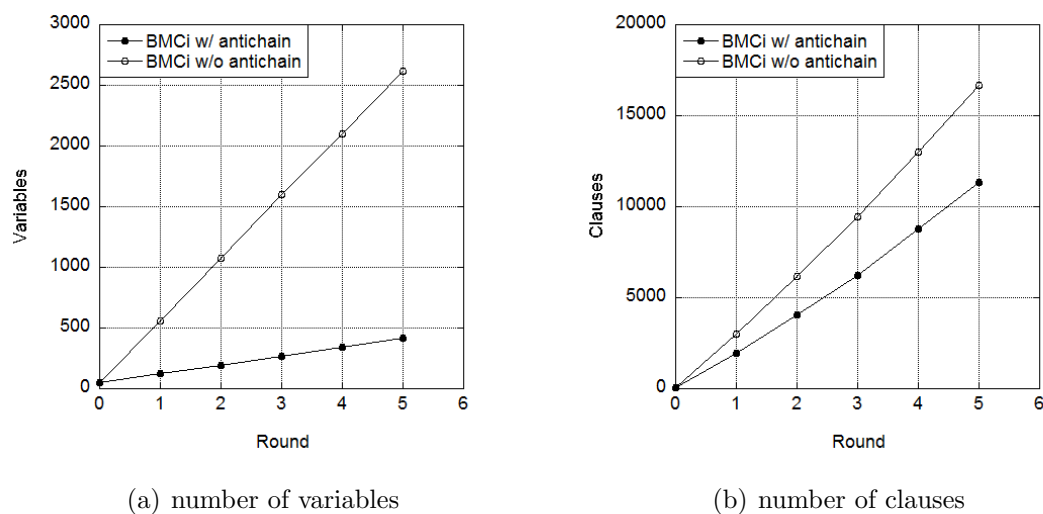


Figure 6.1: number of variables and clauses from test cases r-random of size 5

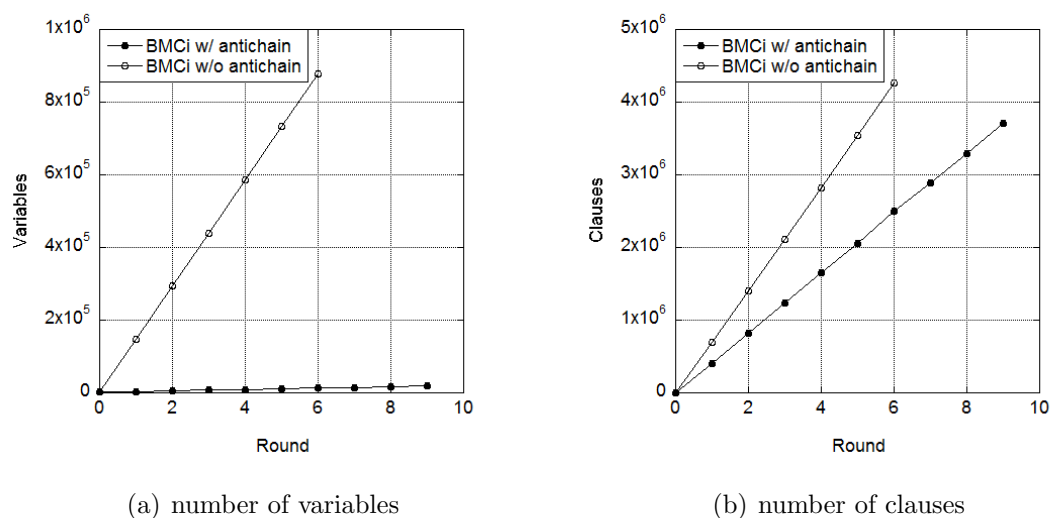


Figure 6.2: number of variables and clauses from test cases r-random of size 30

## Experiment2: universal r-regular random VPAs

We define an r-regular random VPAs in order to test the universal cases as below:

**Definition 16** (r-regular random). *An r-regular random VPA is generated by: first generate states, add transitions labeled with every input symbol (and stack symbol in case of return alphabet) to every state, and randomly generate destination for each transition. The density of transitions  $r$  depends on state and input symbol,  $r(q, a)$  is a number of transitions from state  $q$  reading input symbol  $a$  for  $a \in \Sigma_c$  and  $a \in \Sigma_i$ , and  $r(q, a, \gamma)$  is a number of transitions from state  $q$  reading input symbol  $a$  and top of stack is  $\gamma$  for  $a \in \Sigma_r$ . Intuitively, for VPAs generated with r-regular random, every state must have  $r$  transitions with every input symbol (and stack symbol in case of return alphabet).*

During the experiments, we set the density of final states and initial states  $f = i = 1$ , and  $r(q, a) = r(q, b, \gamma) = r(q, c) = 2$  for each  $a \in \Sigma_c, b \in \Sigma_r, c \in \Sigma_i$  and  $\gamma \in \Gamma$ . This means each state  $q$  has totally 20 transitions from it.

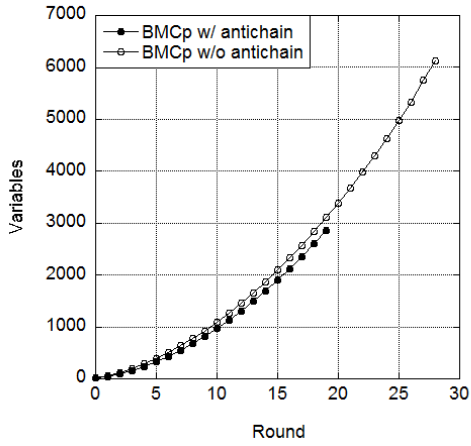
VPAs generated by r-regular random parameters is always universal, since every state is a final state and has transitions for every input symbol and stack symbol.

Table 6.2 shows experimental results on 50 r-regular random VPAs with the timeout set to 300 seconds. We tested only BMCp, because BMCi is not complete. Unfortunately, our current BMCp implementations can solve instances of only 2 and 3 states. Nevertheless, we can see a benefit of antichain algorithm clearly. BMCp with antichain algorithm outperforms BMCp without antichain algorithm.

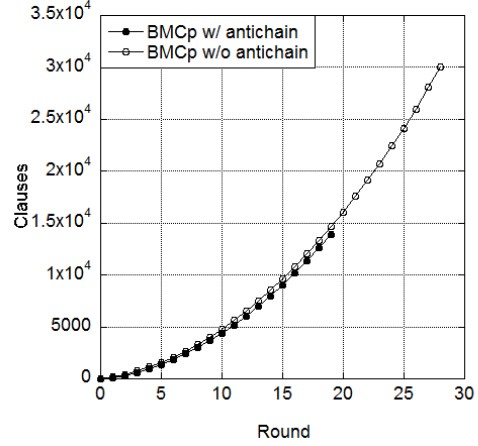
	number of states	
BMCp w/ antichain	2	3
number of success	46	14
number of timeout	4	36
total time(sec)	56	1062
	number of states	
BMCp w/o antichain	2	3
number of success	36	0
number of timeout	14	0
total time(sec)	257	-

Table 6.2: Universality checking for universal VPA generated by r-regular random (50 automata for each sample)

BMC with antichain algorithm is faster than BMC without antichain algorithm from the



(a) number of variables



(b) number of clauses

Figure 6.3: number of variables and clauses from test cases r-regular random of size 2

following two reasons:

- Antichain applied to transitions (Section 5.2) makes the number of variables and clauses generated by CNF conversion fewer. This result enables BMC to search with deeper bound. As shown in Figure 6.1 and 6.2, the number of the bound for BMC with antichain until timeout is greater than that without antichain.
- Antichain applied to saturation checking (Section 5.5) makes  $\mathcal{P}$ -automaton convergence faster. As shown in Figure 6.3, the number of the bound for BMC with antichain until convergence is less than that without antichain. Note that the number of variables and clauses are not much different, since the number of states and transitions of VPAs are few.

*Remark.* Figure 6.4 refers to the most recent results of other tools (Fig. 2 of [14]). Despite differences in test cases and machine, we have to admit that our current algorithms and implementations are still behind.

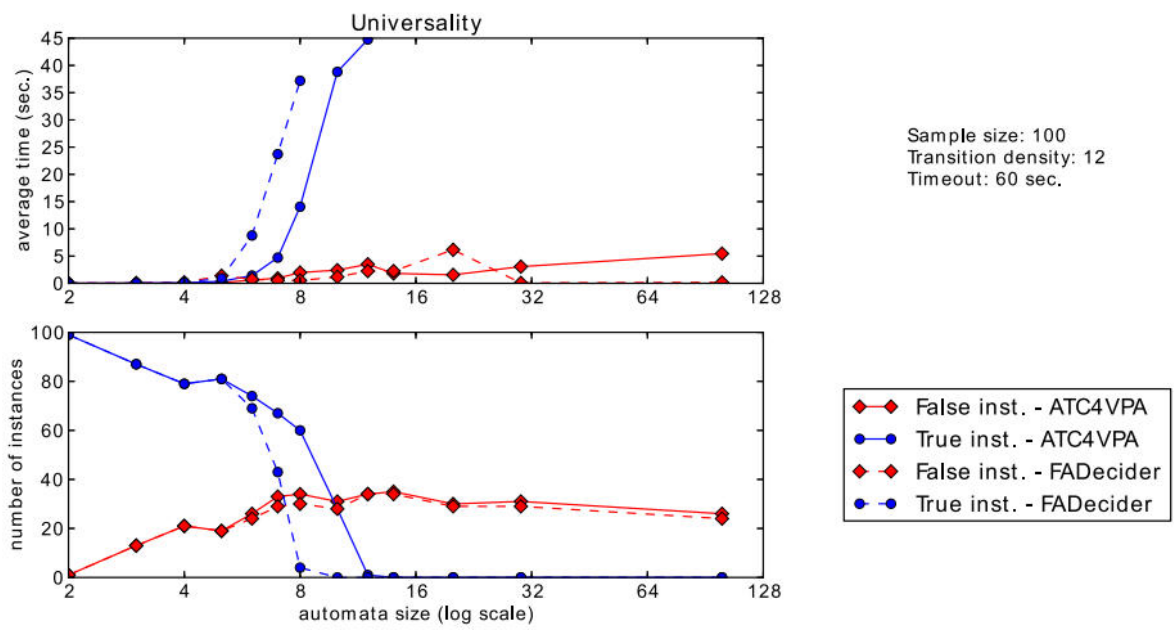


Figure 6.4: Results of other tools (Fig. 2 of [14])

# Chapter 7

## Related work

The concept of antichains is firstly applied to the universality and inclusion checking of finite automata and emptiness checking of alternating finite automata in [19]. However, their tool called ALASKA [20] seems not being actively developed anymore. Later the concept of antichains is extended to tree automata [13] and Büchi automata [21]. Recently, antichain is also applied to equivalence checking of finite automata [11].

Visibly pushdown automata were firstly proposed by Alur and Madhusudan in [5] and investigated in [4]. Before that it seems this type of automata was studied under the name of input driven automata [39, 24]. The visibly pushdown class nicely extends the closure property of finite automata. This is mainly because VPA can be performed determinization.

The universality checking of VPA requires  $O(2^{n^2})$  because of determinization procedure [38]. The first known implementation VPAlib [32] only works with just few states. Currently, there are several tools solving this problem, e.g.,

- **OpenNWA** a nested-word automaton library that provides the standard boolean operations [23],
- **VPAChecker** a package based on VPAlib, deciding universality and inclusion of VPAs by using on-the-fly and antichain algorithm [37],
- **FADecider** a package for deciding universality and inclusion checking of VPAs by using Ramsey-based methods [28],
- **ATC4VPA** a package for deciding universality and inclusion checking of VPAs by encoding VPAs as an trees acceptors and using antichain algorithm [14].

From above tools, only VPAChecker and ATC4VPA are only tools that using antichain.

However, VPAchecker seems to have some bugs reported in [14].

BMC was introduced by Biere [9] as a SAT-based efficient technique for finding bugs. It became a well-known model checking method shortly after its introduction. The BMC technique that is most related to our work, is BMC for checking reachability of pushdown system [8] using summarization [34, 7].



# Chapter 8

## Conclusion and future works

In our article, we proposed two algorithms to tackle the universality checking of VPAs. These algorithms combine antichain and BMC technique in order to overcome state space explosion and optimize size of search space. We unrolled determinization procedure by transitions of  $\mathcal{P}$ -automaton transition based on input word length and  $\mathcal{P}$ -automaton saturation process. We implemented our algorithms and evaluated with existing antichain algorithm. Although experiments show that our current algorithm implementations are slower. They show that an antichain algorithm boosts BMC than a standard BMC without antichain algorithm. This is the first attempt of BMC technique for the universality checking of VPAs, and we expect future improvements of CNF encoding.

This work suggests several future works. We first plan to continue working on BMC approach. We should emphasize that we need to improve our algorithms as well as implementations to be able to check larger examples. At this moment, the encoding of determinized VPA and  $\mathcal{P}$ -automaton are rather naive (e.g., encoding set by bit representation). That would be a reason why our implementations are not fast. It would be interesting to explore a more compact encoding or a more effective method to unroll VPA. We also would like to extend our algorithms to inclusion checking of VPAs. Another challenge would be to consider combination of antichain and simulation, since this combination is successful for finite automata and tree automata [1]. Simulation can help antichain to reduce a large portion of unnecessary search space. The last would be applications to practical case studies (rather than randomly generated). XML processing and/or program verification will be such examples.

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