

Formal Verification of Observational Transition Systems with CafeOBJ CITP

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Outline of Lecture

Two mutual exclusion protocols as examples

2P-Mutex – A simple two-process mutual exclusion protocol

Qlock – Dijkstra's binary semaphore

are used to describe how to verify that observational transition systems (OTSs) enjoy invariant properties with

CafeOBJ Constructor-based Inductive Theorem Prover (CITP)

a proof assistant for CafeOBJ

based on how proof scores have been written.

Outline of CafeOBJ CITP

```
open 2P-MUTEX . red inv1(S) . close
```

```
open 2P-MUTEX .
:goal {
eq [inv1 :nonexec] : inv1(S:Sys) = true . }
```

```
open 2P-MUTEX .
red inv1(init) .
close
```

```
... ...
```

```
:ind on (S:Sys)
:apply (si)
```

```
open 2P-MUTEX . op s :-> Sys .
eq [:nonexec] : inv1(s) = true .
red inv1(enter1(s)) .
close
```

```
:apply (rd)
```

```
open 2P-MUTEX . op s :-> Sys .
eq [:nonexec] : inv1(s) = true .
red inv1(s) implies inv1(enter1(s)) .
close
```

```
:imp [inv1] .
```

Outline of CafeOBJ CITP

```
open 2P-MUTEX . op s :-> Sys .
eq [:nonexec] : inv1(s) = true .
red inv1(s) implies inv1(enter1(s)) .
close
```

```
:def cpb1 = :ctf{ eq pc2(S#Sys) = rs . }
:apply(csb1)
```

```
:apply (rd)
```

```
:apply (rd)
```

```
open 2P-MUTEX . ...
eq pc2(s) = rs .
red inv1(s) implies inv1(enter1(s)) .
close
```

```
open 2P-MUTEX . ...
eq (pc2(s) = rs) = false .
red inv1(s) implies inv1(enter1(s)) .
close
```

Outline of remaining talk

Review how to specify the two mutual exclusion protocols as OTSs in CafeOB

Verify that the two mutual exclusion protocols enjoy the mutual exclusion properties with CafeOBJ CIP based on how the proof scores have been written to verify that the protocols enjoy the property

How to specify 2P-MUTEX

Observers

op *pc1* : Sys -> Label

op *pc2* : Sys -> Label

Observable values

init  **eq** *pc1*(init) = *rs* .

eq *pc2*(init) = *rs* .

Transitions

Pseudo-code for p_1 :

Loop: "Remainder Section"
rs: **repeat while** p_2 is at *cs*;
 "Critical Section"
cs: do nothing

op *enter1* : Sys -> Sys

op *leave1* : Sys -> Sys

How to specify 2P-MUTEX

$pc1(S) = l \quad S$ $S' = \text{enter1}(S) \quad pc1(S') = cs$
 $pc2(S) = rs$  $\longrightarrow$  $pc2(S') = rs$

ceq $pc1(\text{enter1}(S)) = cs$ **if** $c\text{-enter1}(S)$.
ceq $pc2(\text{enter1}(S)) = pc2(S)$ **if** $c\text{-enter1}(S)$.
ceq $\text{enter1}(S) = S$ **if not** $c\text{-enter1}(S)$.

where $c\text{-enter1}(S)$ is $pc2(S) = rs$

$pc1(S) = cs \quad S$ $S' = \text{leave1}(S) \quad pc1(S') = rs$
 $pc2(S) = l$  $\longrightarrow$  $pc2(S') = l$

ceq $pc1(\text{leave1}(S)) = rs$ **if** $c\text{-leave1}(S)$.
ceq $pc2(\text{leave1}(S)) = pc2(S)$ **if** $c\text{-leave1}(S)$.
ceq $\text{leave1}(S) = S$ **if not** $c\text{-leave1}(S)$.

where $c\text{-leave1}(S)$ is $pc1(S) = cs$

How to specify 2P-MUTEX

$pc1(S) = rs \quad S$ $S' = \text{enter2}(S) \quad pc1(S') = rs$
 $pc2(S) = l$  $\longrightarrow$  $pc2(S') = cs$

ceq $pc1(\text{enter2}(S)) = pc1(S)$ **if** $c\text{-enter2}(S)$.
ceq $pc2(\text{enter2}(S)) = cs$ **if** $c\text{-enter2}(S)$.
ceq $\text{enter2}(S) = S$ **if not** $c\text{-enter2}(S)$.

where $c\text{-enter2}(S)$ is $pc1(S) = rs$

$pc1(S) = l \quad S$ $S' = \text{leave2}(S) \quad pc1(S') = l$
 $pc2(S) = cs$  $\longrightarrow$  $pc2(S') = rs$

ceq $pc1(\text{leave2}(S)) = pc1(S)$ **if** $c\text{-leave2}(S)$.
ceq $pc2(\text{leave2}(S)) = rs$ **if** $c\text{-leave2}(S)$.
ceq $\text{leave2}(S) = S$ **if not** $c\text{-leave2}(S)$.

where $c\text{-leave2}(S)$ is $pc2(S) = cs$

Mutual exclusion property

eq $\text{inv1}(S:\text{Sys}) = \text{not } (\text{pc1}(S) = \text{cs} \text{ and } \text{pc2}(S) = \text{cs})$.

We tackle the goal:

2P-MUTEX $\vdash (\forall S:\text{Sys}) \text{inv1}(S) = \text{true}$.

Some commands of CafeOBJ CITP

:goal { *eqs* } where *eqs* is a set of conditional equations (or sentences) to prove

E.g. *eqs* is **eq** [inv1 :**nonexec**] : $\text{inv1}(S:\text{Sys}) = \text{true}$.

or **eq** [inv1 :**nonexec**] : $\text{inv1}(S:\text{Sys}, l:\text{Pid}, j:\text{Pid}) = \text{true}$.

eq [inv2 :**nonexec**] : $\text{inv2}(S:\text{Sys}, l:\text{Pid}) = \text{true}$.

A module *Spec* is supposed to be opened or selected.

An initial current goal *Spec* $\vdash$ *eqs* is defined.

:ind on *X:S* where *X* is a variable of a constrained sort *S*

A variable *X* is specified to which simultaneous structural induction is applied.

Some commands of CafeOBJ CITP

:apply (si)

Simultaneous structural induction is applied to the current goal on the variable X specified with **:ind on**, replacing the current goal with n sub-goals, where n is the number of the constructors of the sort S of X , and introducing fresh constants for the non-constant constructors.

:apply (tc)

Each variable in the sentences of the current goal is replaced with a fresh constant. If the current goal has two or more sentences, say n , then the goal is replaced with n sub-goals.

Some commands of CafeOBJ CITP

#def base- eq_i = :ctf { eq $l_i = r_i$. }

A base for case splitting is defined. The current goal is split into two sub-goals for $l_i = r_i$ and $(l_i = r_i) = \text{false}$, respectively, when it is used by **:apply**.

#def base- ctr_j = :ctf [t_j .] where the sort of t_j is constrained

A base for case splitting is defined. The current goal is split into n sub-goals, where n is the number of the constructors of the sort of t_j when **base- ctr_j** is used by **:apply**.

:apply ($b_1 \dots b_m$) where b_k is **base- eq_k** or **base- ctr_k** and splits the goal into n_k sub-goals

The current goal is split into $n_1 \times \dots \times n_m$ sub-goals.

Some commands of CafeOBJ CITP

:imp [*label*] by { $x_1 <- t_1 ; \dots x_n <- t_n ;$ }

where *label* is the label of an equation that is in the form **eq** $l = \text{true}$., x_k is a variable in l , and t_k is a ground term

Let gl be the ground term obtained by replacing each x_k with t_k in l , and gs be the sentence to prove in the current goal.

The current goal is replaced with the goal in which gl implies gs is the sentence to prove.

:imp [*label*] .

In the case the equation referred by *label* has no variables.

:apply (**rd**)

The sentence to prove in the current goal is reduced. If it reduces to true, then the current goal is discharged.

Some commands of CafeOBJ CITP

:show proof

An outline of the proof conducted so far is shown. The current goal is marked with $>$.

:desc proof

The sub-goals generated so far are shown.

:desc .

The current sub-goal is shown.

Proving with CafeOBJ CIP

The specification under verification is 2P-MUTEX in which the sentence to prove is described.

```
open 2P-MUTEX .
:goal {
eq [inv1 :nonexec] : inv1(S:Sys) = true .
}
```

SI is applied to the current goal on S:Sys, replacing the current goal with five sub-goals because of the five constructors.

```
:ind on (S:Sys)
:apply (si)
```

```
open 2P-MUTEX .
red inv1(init) .
close
```



```
-- init
:apply (rd)
```

The goal is discharged with **:apply (rd)**.

Note that a comment starts with **--** until the end of the line.

Proving with CafeOBJ CIP

```
open 2P-MUTEX .
op s : -> Sys .
eq [:nonexec] : inv1(s) = true .
eq pc2(s) = rs .
red inv1(s) implies inv1(enter1(s)) .
close
open 2P-MUTEX .
op s : -> Sys .
eq [:nonexec] : inv1(s) = true .
eq (pc2(s) = rs) = false .
red inv1(s) implies inv1(enter1(s)) .
close
```



```
-- enter1(S#Sys)
:imp [inv1] .
:def ctf-0 = :ctf{ eq pc2(S#Sys) = rs . }
:apply(ctf-0)
:apply (rd)
:apply (rd)
```

Each goal is discharged with **:apply (rd)**.

Proving with CafeOBJ CIP

<pre> open 2P-MUTEX . op s : -> Sys . eq [:nonexec] : inv1(s) = true . eq pc1(s) = cs . red inv1(s) implies inv1(leave1(s)) . close open 2P-MUTEX . op s : -> Sys . eq [:nonexec] : inv1(s) = true . eq (pc1(s) = cs) = false . red inv1(s) implies inv1(leave1(s)) . close </pre>	 	<pre> -- leave1(S#Sys) :imp [inv1] . :def ctf-1 = :ctf{ eq pc1(S#Sys) = cs . } :apply(ctf-1) :apply (rd) :apply (rd) </pre>
--	--	---

Each goal is discharged with **:apply (rd)**.

Proving with CafeOBJ CIP

<pre> open 2P-MUTEX . op s : -> Sys . eq [:nonexec] : inv1(s) = true . eq pc1(s) = rs . red inv1(s) implies inv1(enter2(s)) . close open 2P-MUTEX . op s : -> Sys . eq [:nonexec] : inv1(s) = true . eq (pc1(s) = rs) = false . red inv1(s) implies inv1(enter2(s)) . close </pre>	 	<pre> -- enter2(S#Sys) :imp [inv1] . :def ctf-2 = :ctf{ eq pc1(S#Sys) = rs . } :apply(ctf-2) :apply (rd) :apply (rd) </pre>
--	--	---

Each goal is discharged with **:apply (rd)**.

Proving with CafeOBJ CIP

```

open 2P-MUTEX .
  op s : -> Sys .
  eq [:nonexec] : inv1(s) = true .
  eq pc2(s) = cs .
  red inv1(s) implies inv1(leave2(s)) .
close
open 2P-MUTEX .
  op s : -> Sys .
  eq [:nonexec] : inv1(s) = true .
  eq (pc2(s) = cs) = false .
  red inv1(s) implies inv1(leave2(s)) .
close

```



```

-- leave2(S#Sys)
:imp [inv1] .
:def ctf-3 = :ctf{ eq pc2(S#Sys) = cs . }
:apply(ctf-3)
:apply (rd)
:apply (rd)

```

Each goal is discharged with **:apply (rd)**.

How to specify Qlock

Observers

```

op queue : Sys -> Queue
op pc : Sys Pid -> Label

```

Observable values

```

init
eq queue(init) = empty .
eq pc(init, I) = rs .

```

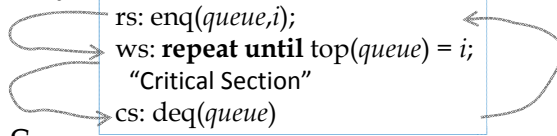
Transitions

```

op want : Sys Pid -> Sys
op try : Sys Pid -> Sys
op exit : Sys Pid -> Sys

```

Loop: "Remainder Section"
 rs: enq(queue, i);
 ws: repeat until top(queue) = i;
 "Critical Section"
 cs: deq(queue)



How to specify Qlock

ceq $\text{pc}(\text{want}(S,I),I) = (\text{if } I = J \text{ then } \text{ws} \text{ else } \text{pc}(S,I) \text{ fi}) \text{ if } \text{c-want}(S,I) .$
ceq $\text{queue}(\text{want}(S,I)) = \text{enq}(\text{queue}(S),I) \text{ if } \text{c-want}(S,I) .$
ceq $\text{want}(S,I) = S \text{ if not } \text{c-want}(S,I) .$

where $\text{c-want}(S,I)$ is $\text{pc}(S,I) = \text{rs}$

ceq $\text{pc}(\text{try}(S,I),I) = (\text{if } I = J \text{ then } \text{cs} \text{ else } \text{pc}(S,I) \text{ fi}) \text{ if } \text{c-try}(S,I) .$
eq $\text{queue}(\text{try}(S,I)) = \text{queue}(S) .$
ceq $\text{try}(S,I) = S \text{ if not } \text{c-try}(S,I) .$

where $\text{c-try}(S,I)$ is $\text{pc}(S,I) = \text{ws}$ and $\text{top}(\text{queue}(S)) = I$

ceq $\text{pc}(\text{exit}(S,I),I) = (\text{if } I = J \text{ then } \text{rs} \text{ else } \text{pc}(S,I) \text{ fi}) \text{ if } \text{c-exit}(S,I) .$
ceq $\text{queue}(\text{exit}(S,I)) = \text{deq}(\text{queue}(S)) \text{ if } \text{c-exit}(S,I) .$
ceq $\text{exit}(S,I) = S \text{ if not } \text{c-exit}(S,I) .$

where $\text{c-exit}(S,I)$ is $\text{pc}(S,I) = \text{cs}$

Mutual exclusion property

eq $\text{inv1}(S:\text{Sys},I:\text{Pid},J:\text{Pid})$
 $= ((\text{pc}(S,I) = \text{cs} \text{ and } \text{pc}(S,J) = \text{cs}) \text{ implies } I = J) .$

Since we have written proof scores to verify that Qlock enjoys the property, we know the following lemma:

eq $\text{inv2}(S:\text{Sys},I:\text{Pid})$
 $= (\text{pc}(S,I) = \text{cs} \text{ implies } \text{top}(\text{queue}(S)) = I) .$

Then, we tackle the following goal:

QLOCK $\vdash \{\text{inv1}(S,I,J) = \text{true}, \text{inv2}(S,I) = \text{true}\}$

in which there are two sentences to prove.

Proving with CafeOBJ CIP

```
open QLOCK .
  red inv1(S:Sys,I:Pid,J:Pid) .
  red inv2(S:Sys,I:Pid) .
close
```

SI on S:Sys

want(s, k)

exit(s, k)

...

...

init

try(s, k)

```
open QLOCK .
  red inv1(init,I:Pid,J:Pid) .
  red inv2(init,I:Pid) .
close
```

```
open QLOCK .
  op k : -> Pid .
  eq [:nonexec] : inv1(s, I:Pid, J:Pid) = true .
  eq [:nonexec] : inv2(s, I:Pid) = true .
  red inv1(try(s,k), I:Pid, J:Pid) .
  red inv2(try(s,k), I:Pid) .
close
```

```
open QLOCK .
```

```
:goal {
```

```
eq [inv1 :nonexec] :
```

```
  inv1(S:Sys,I:Pid,J:Pid) = true .
```

```
eq [inv2 :nonexec] :
```

```
  inv2(S:Sys,I:Pid) = true .
```

```
}
```

```
:ind on (S:Sys)
```

```
:apply (si)
```

Proving with CafeOBJ CIP

```
open QLOCK .
  red inv1(init,I:Pid,J:Pid) .
  red inv2(init,I:Pid) .
close
```

TC

inv1

inv2

```
open QLOCK .
  ops i j : -> Pid .
  red inv1(init, i, j) .
close
```

```
open QLOCK .
  op i : -> Pid .
  red inv2(init, i) .
close
```

```
-- init
```

```
:apply (tc)
```

```
-- inv1
```

```
:apply (rd)
```

```
-- inv2
```

```
:apply (rd)
```

Proving with CafeOBJ CIP

```

open QLOCK .
  op k : -> Pid .
  eq [:nonexec] : inv1(s, l:Pid, j:Pid) = true .
  eq [:nonexec] : inv2(s, l:Pid) = true .
  red inv1(try(s,k), l:Pid, j:Pid) .
  red inv2(try(s,k), l:Pid) .
close

```

-- try(S#Sys, S#Pid)
:apply (tc)

TC

inv2

inv1

```

open QLOCK .
  ops i k : -> Pid .
  eq [:nonexec] : inv1(s, l:Pid, j:Pid) = true .
  eq [:nonexec] : inv2(s, l:Pid) = true .
  red inv2(try(s,k), i) .
close

```

```

open QLOCK .
  ops i j k : -> Pid .
  eq [:nonexec] : inv1(s, l:Pid, j:Pid) = true .
  eq [:nonexec] : inv2(s, l:Pid) = true .
  red inv1(try(s,k), i, j) .
close

```

Proving with CafeOBJ CIP

```

open QLOCK .
  ops i j k : -> Pid .
  eq [:nonexec] : inv1(s, l:Pid, j:Pid) = true .
  eq [:nonexec] : inv2(s, l:Pid) = true .
  red inv1(try(s,k), i, j) .
close

```

IMP with $\text{inv1}(s, i, j)$

```

open QLOCK .
  ops i j k : -> Pid .
  eq [:nonexec] : inv1(s, l:Pid, j:Pid) = true .
  eq [:nonexec] : inv2(s, l:Pid) = true .
  red inv1(s, i, j) implies inv1(try(s,k), i, j) .
close

```

CS based on $\text{pc}(s, k) = \text{ws}$ $\text{pc}(s, k) = \text{ws}$

```

open QLOCK .
  op s : -> Sys . op i j k : -> Pid .
  eq [:nonexec] : inv1(s, l:Pid, j:Pid) = true .
  eq [:nonexec] : inv2(s, l:Pid) = true .
  eq pc(s, k) = ws .
  red inv1(s, i, j) implies inv1(try(s,k), i, j) .
close

```

-- inv1

:imp [inv1]

by {l:Pid <- l@Pid ;
j:Pid <- j@Pid ;}

:def csb6 =

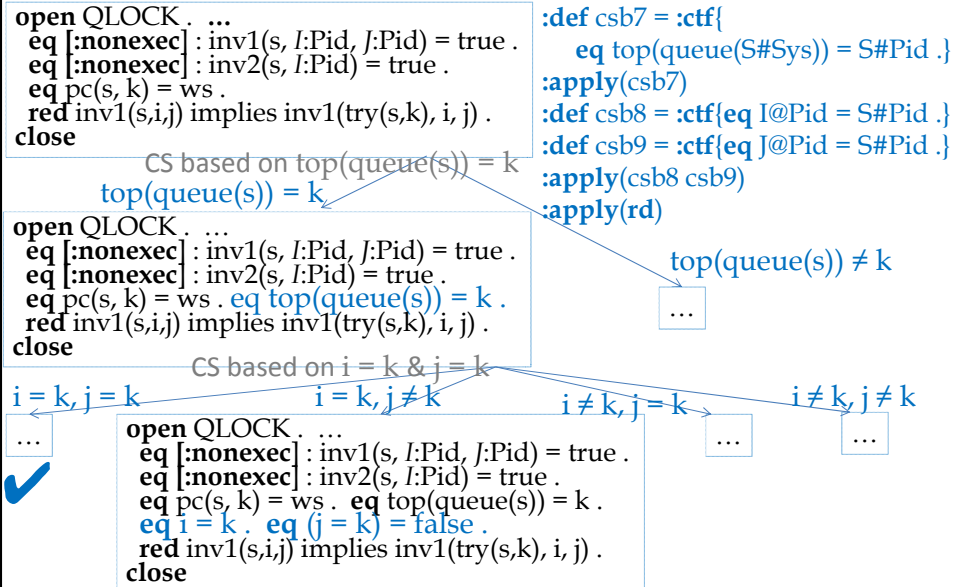
:ctf{eq pc(S#Sys, S#Pid) = ws .}

:apply(csb6)

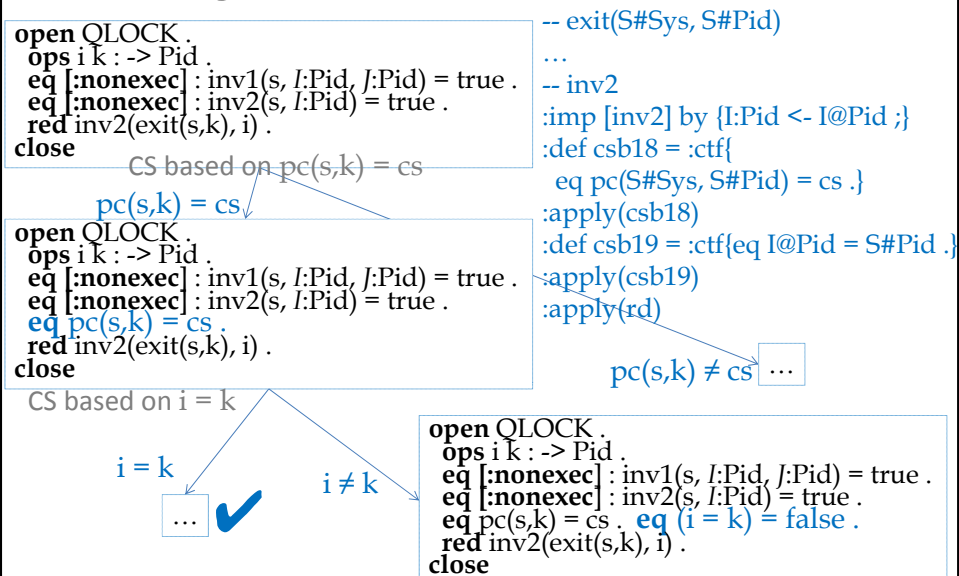
 $\text{pc}(s, k) \neq \text{ws}$

...

Proving with CafeOBJ CITP



Proving with CafeOBJ CITP



Proving with CafeOBJ CIP

```

open QLOCK .
ops i k : -> Pid .
eq [:nonexec] : inv1(s, l:Pid, j:Pid) = true .
eq [:nonexec] : inv2(s, l:Pid) = true .
red inv2(exit(s,k), i) .
close

```

-- exit(S#Sys, S#Pid)
 ...
 -- inv2
 :imp [inv2] by {l:Pid <- I@Pid ;}

IMP with inv2(s,i)

```

open QLOCK .
ops i k : -> Pid .
eq [:nonexec] : inv1(s, l:Pid, j:Pid) = true .
eq [:nonexec] : inv2(s, l:Pid) = true .
red inv2(s,i) implies inv2(exit(s,k), i) .
close

```

Proving with CafeOBJ CIP

```

open QLOCK .
ops i k : -> Pid .
eq [:nonexec] : inv1(s, l:Pid, j:Pid) = true .
eq [:nonexec] : inv2(s, l:Pid) = true .
red inv2(s,i) implies inv2(exit(s,k), i) .
close

```

:def csb18 = :ctf{
 eq pc(S#Sys, S#Pid) = cs .}
 :apply(csb18)
 :def csb19 = :ctf{eq I@Pid = S#Pid .}
 :apply(csb19)
 :apply(rd)

CS based on $pc(s,k) = cs$

$pc(s,k) = cs$

```

open QLOCK .
ops i k : -> Pid .
eq [:nonexec] : inv1(s, l:Pid, j:Pid) = true .
eq [:nonexec] : inv2(s, l:Pid) = true .
eq pc(s,k) = cs .
red inv2(s,i) implies inv2(exit(s,k), i) .
close

```

$pc(s,k) \neq cs$...

CS based on $i = k$

$i = k$



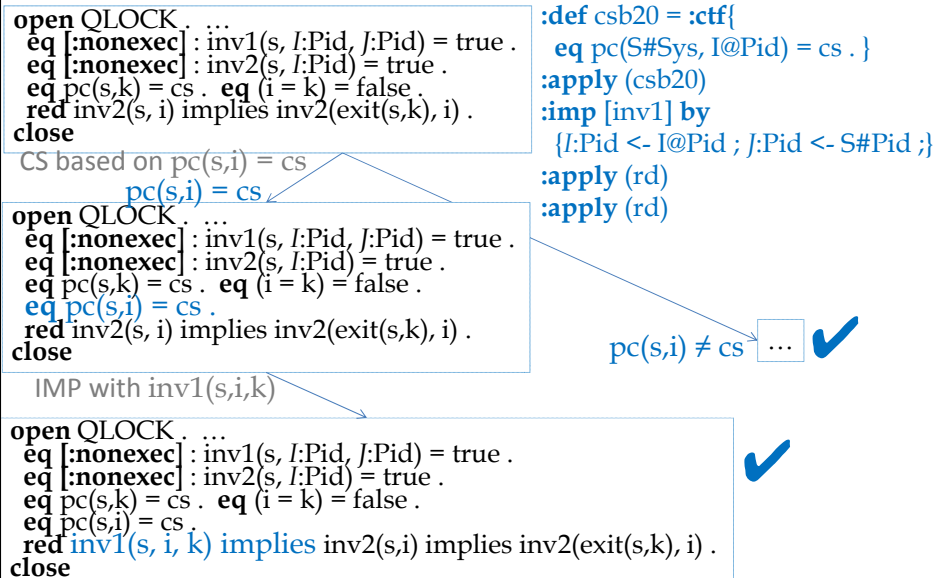
$i \neq k$

```

open QLOCK .
ops i k : -> Pid .
eq [:nonexec] : inv1(s, l:Pid, j:Pid) = true .
eq [:nonexec] : inv2(s, l:Pid) = true .
eq pc(s,k) = cs . eq (i = k) = false .
red inv2(s,i) implies inv2(exit(s,k), i) .
close

```

Proving with CafeOBJ CITP



Summary

Two mutual exclusion protocols as examples

2P-Mutex – A simple two-process mutual exclusion protocol

Qlock – Dijkstra's binary semaphore

have been used to describe how to verify that observational transition systems (OTSs) enjoy invariant properties with

CafeOBJ Constructor-based Inductive Theorem Prover (CITP)

a proof assistant for CafeOBJ

based on how proof scores have been written.